**Figure 1**

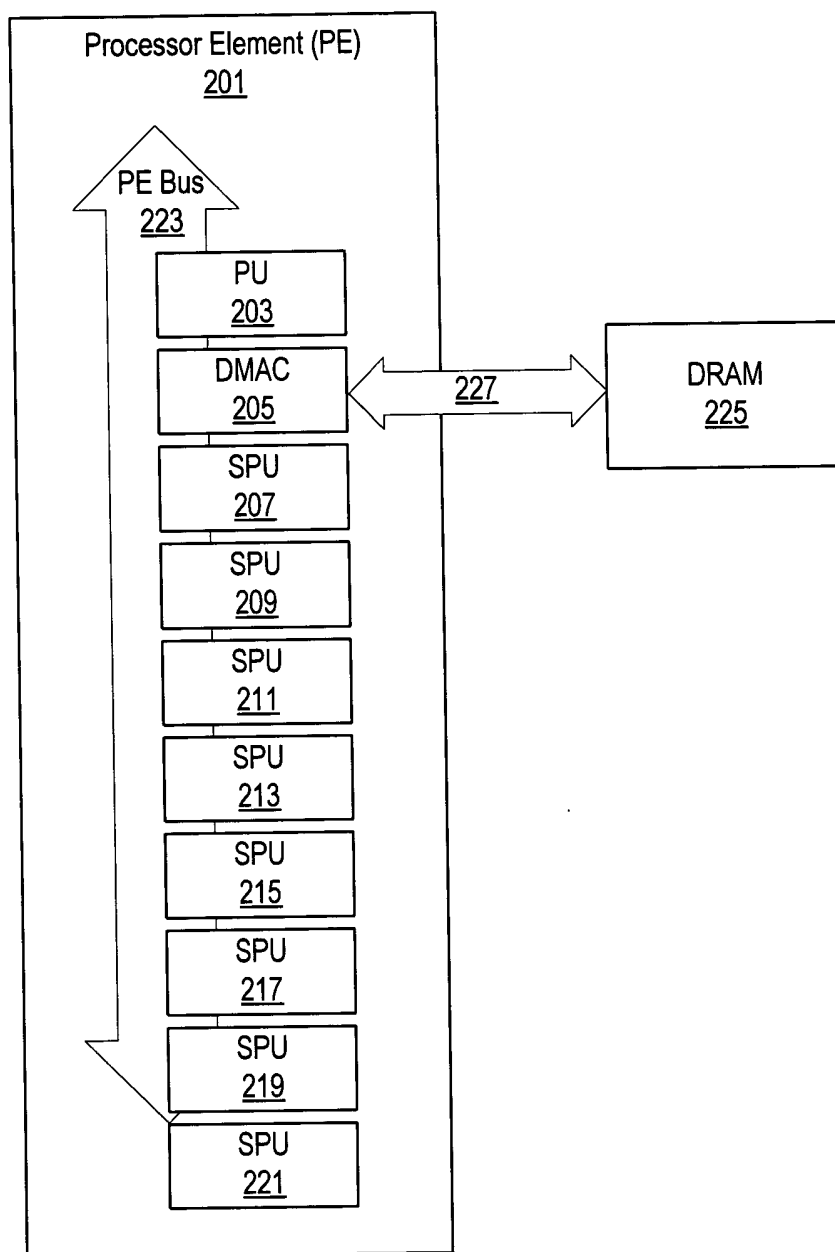
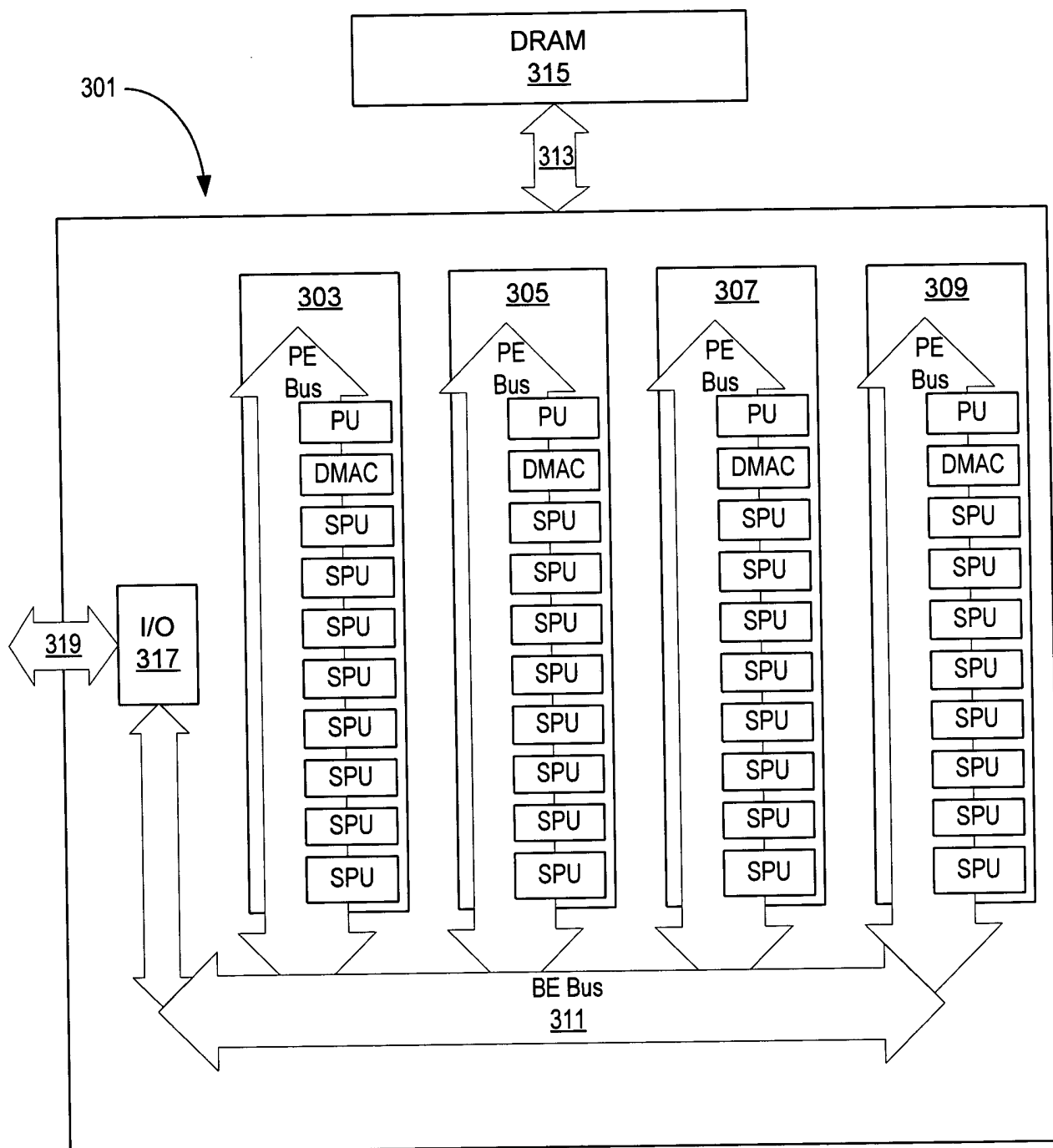
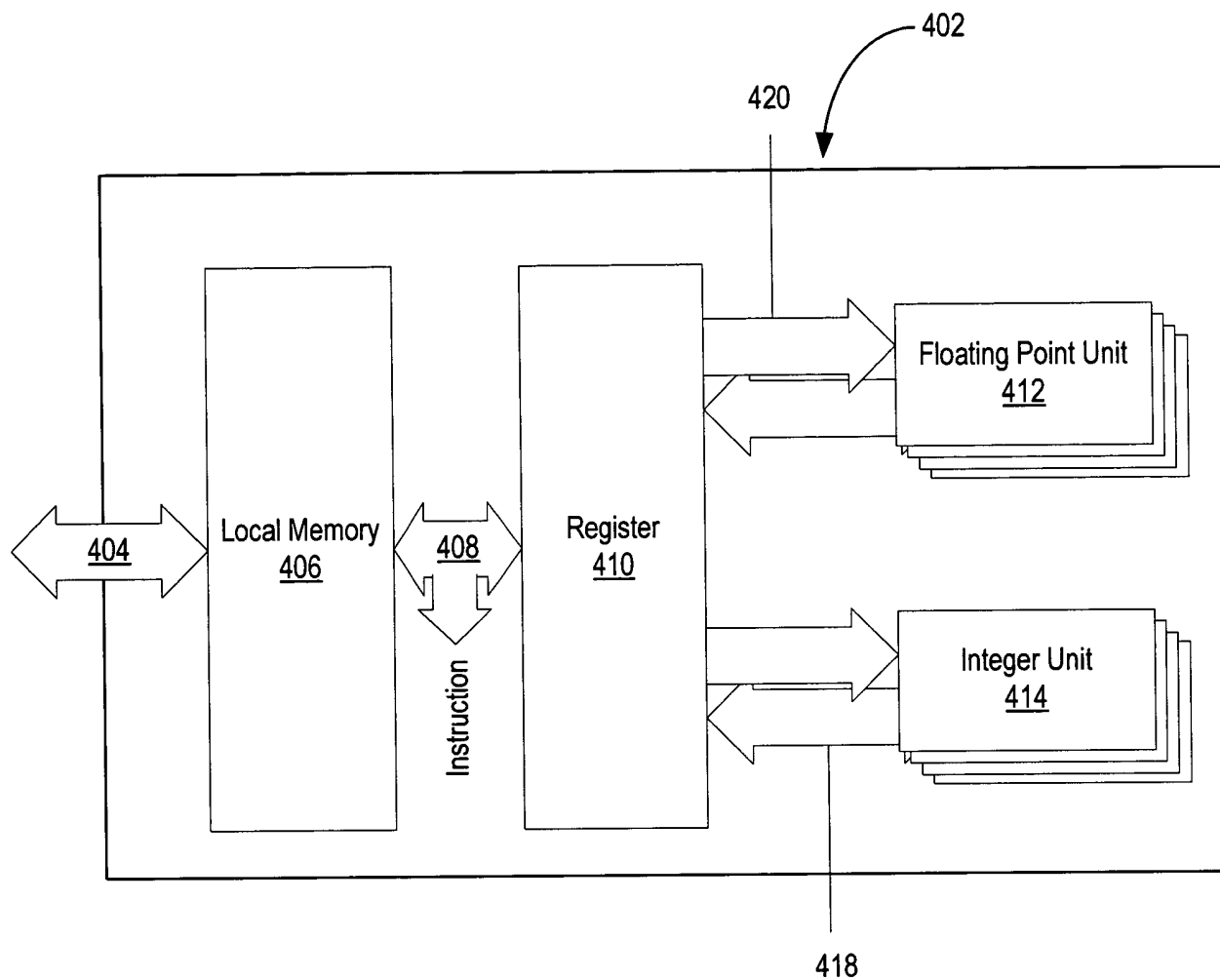


Figure 2

**Figure 3**

**Figure 4**

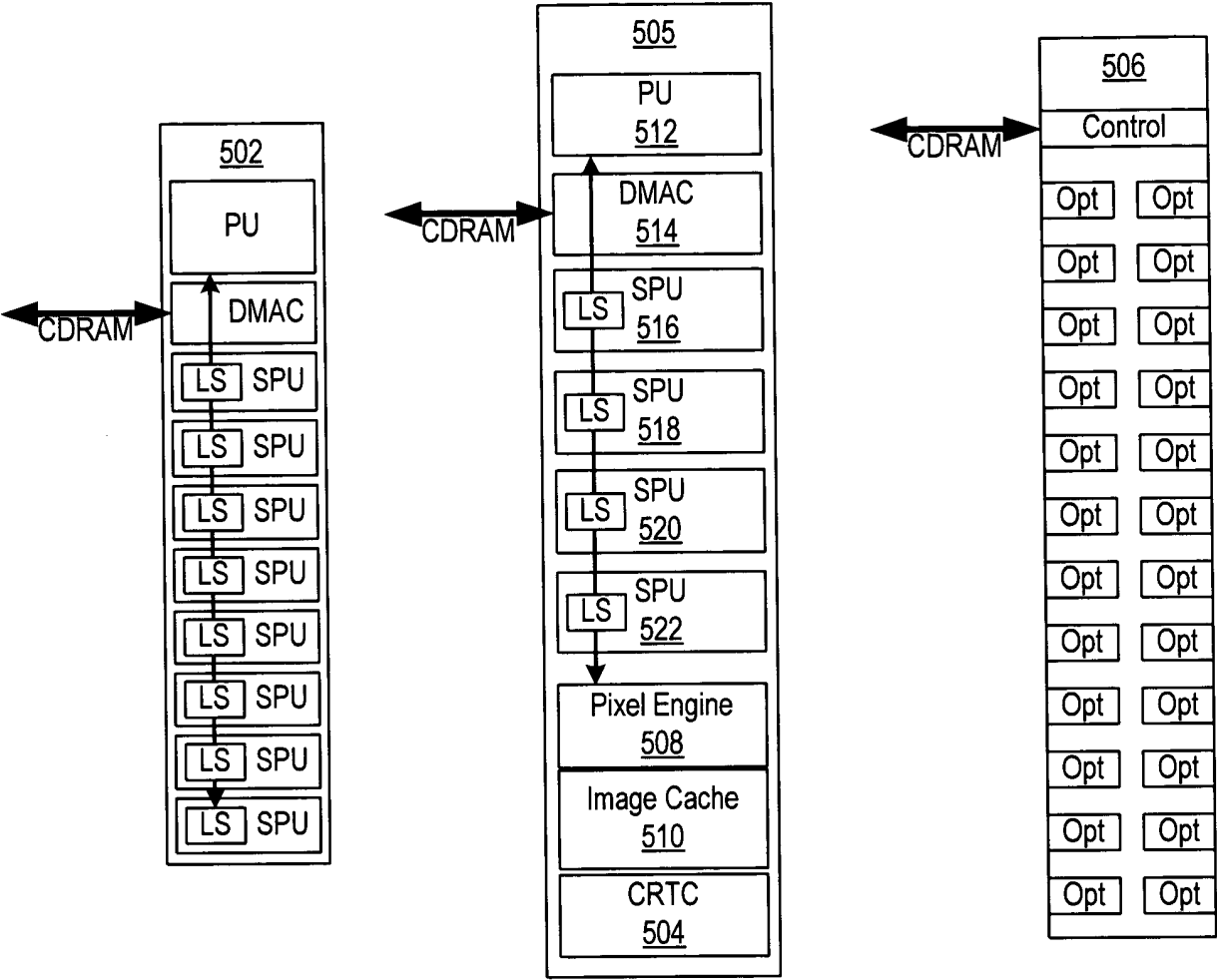


Figure 5

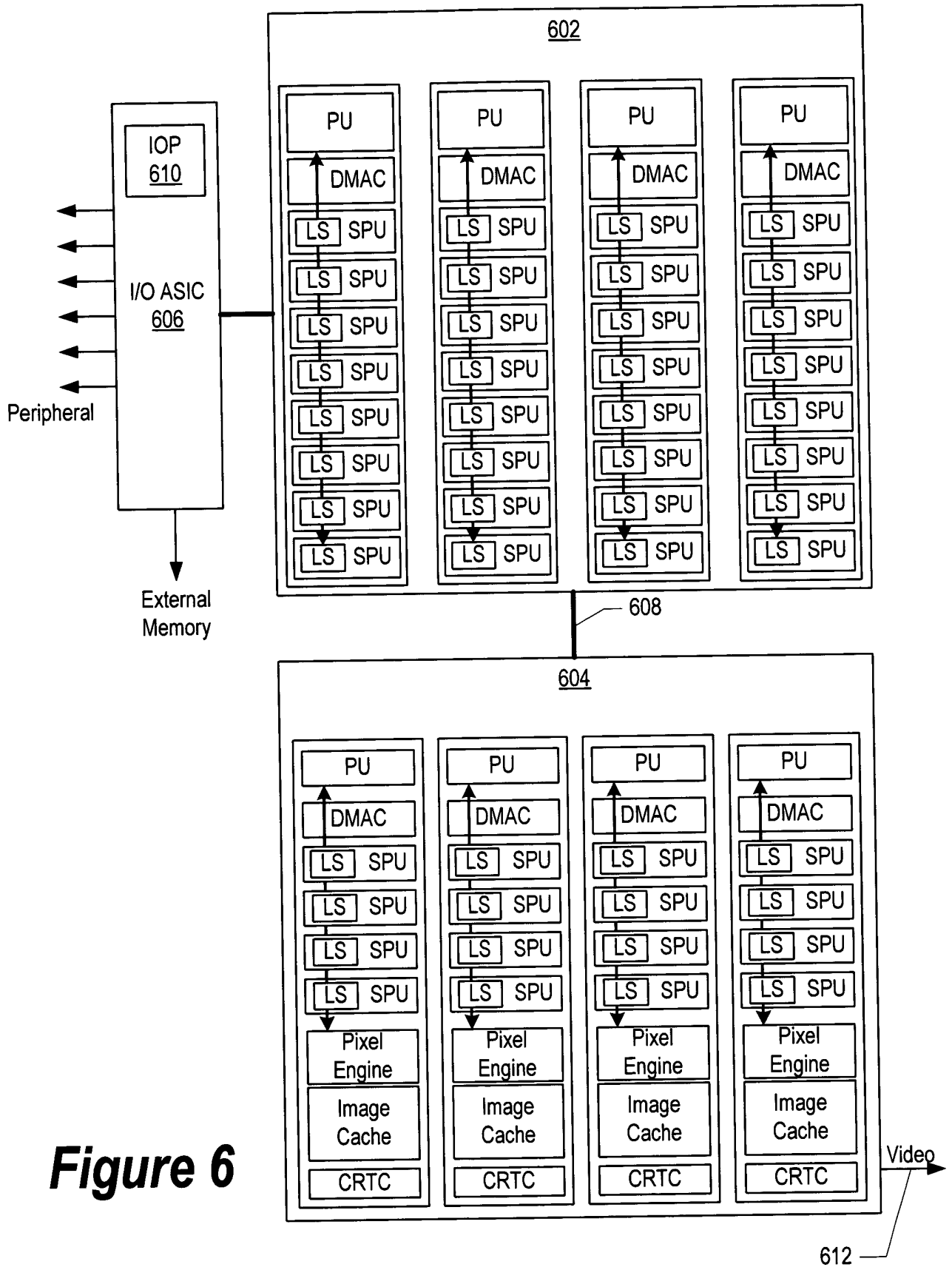
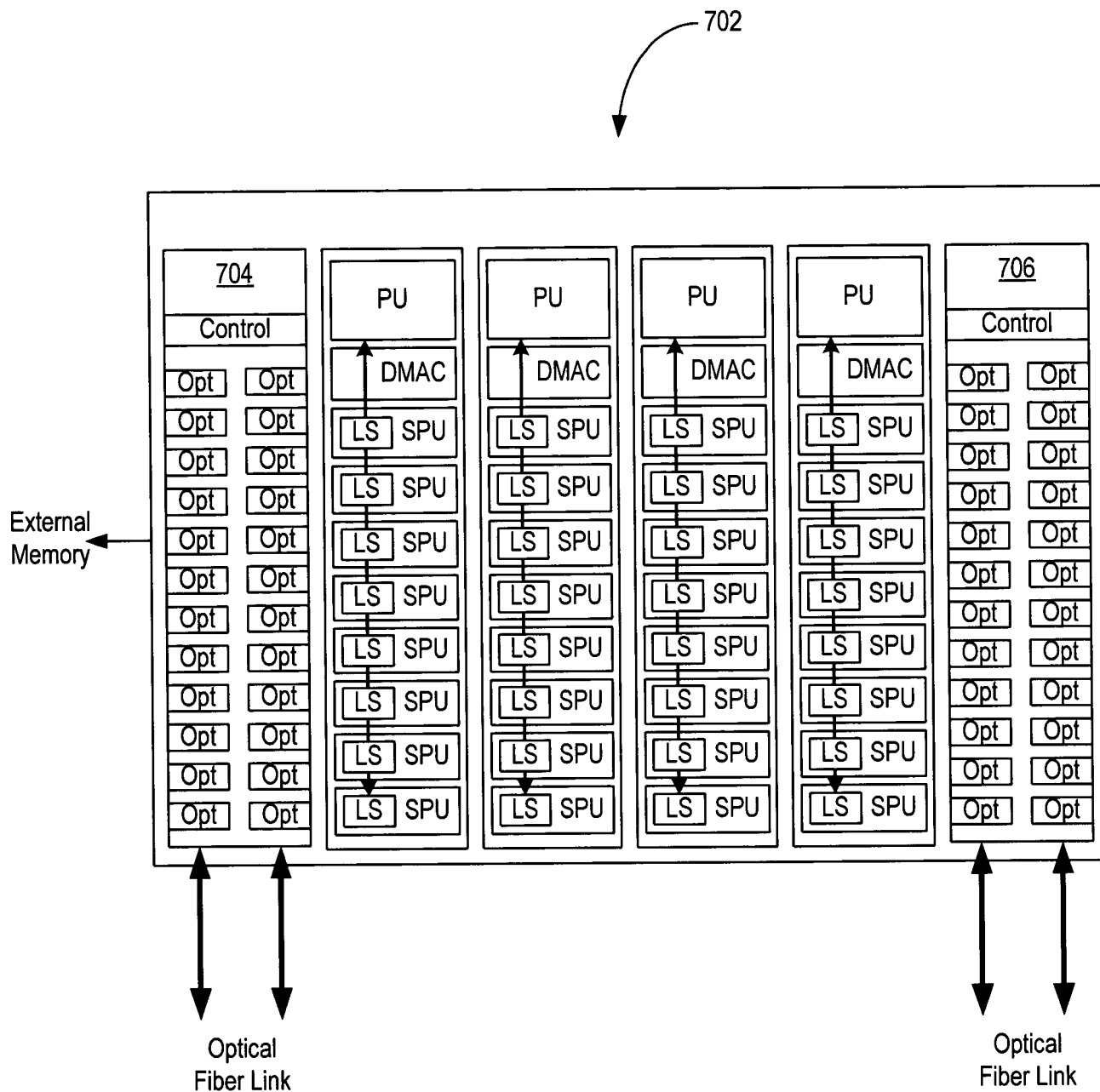
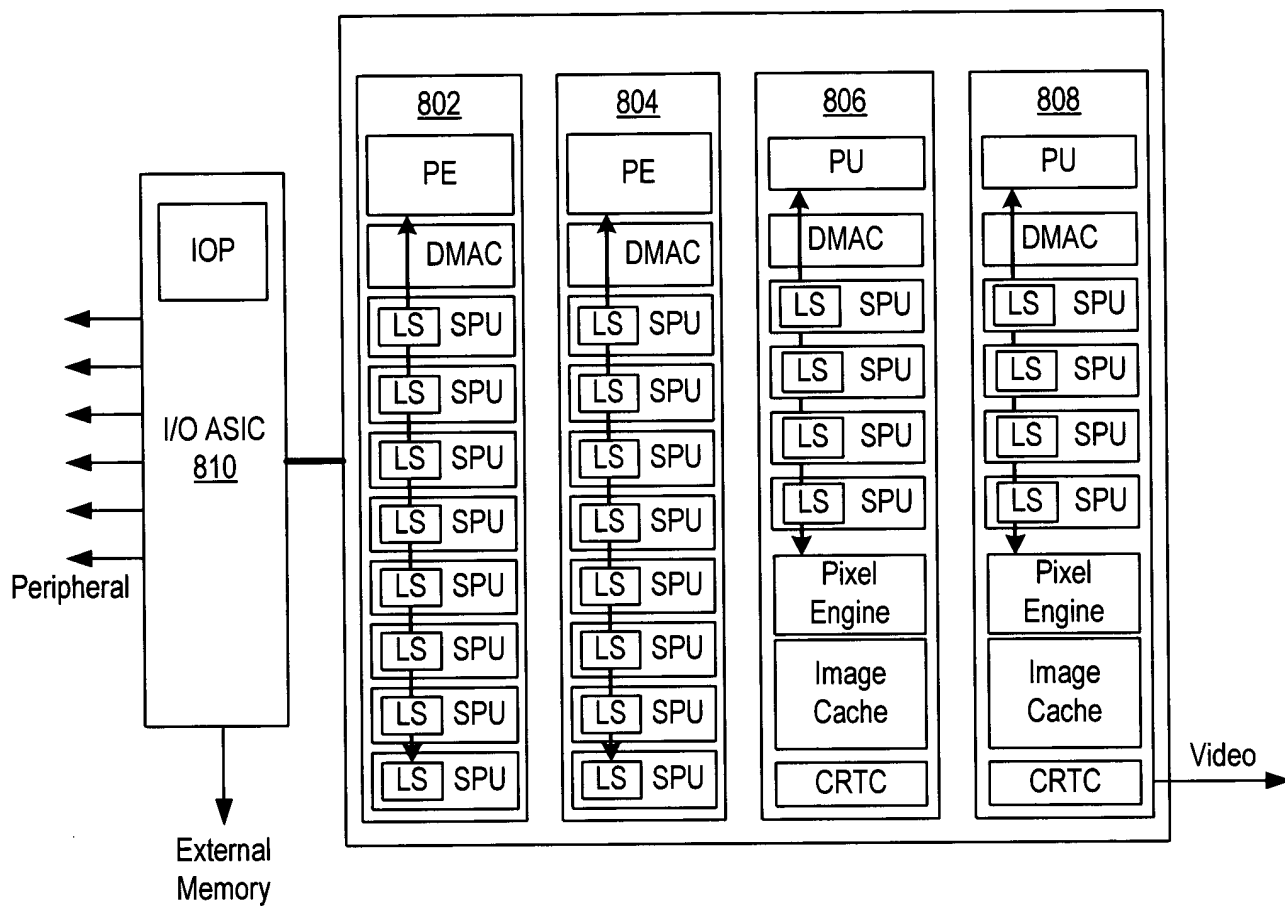
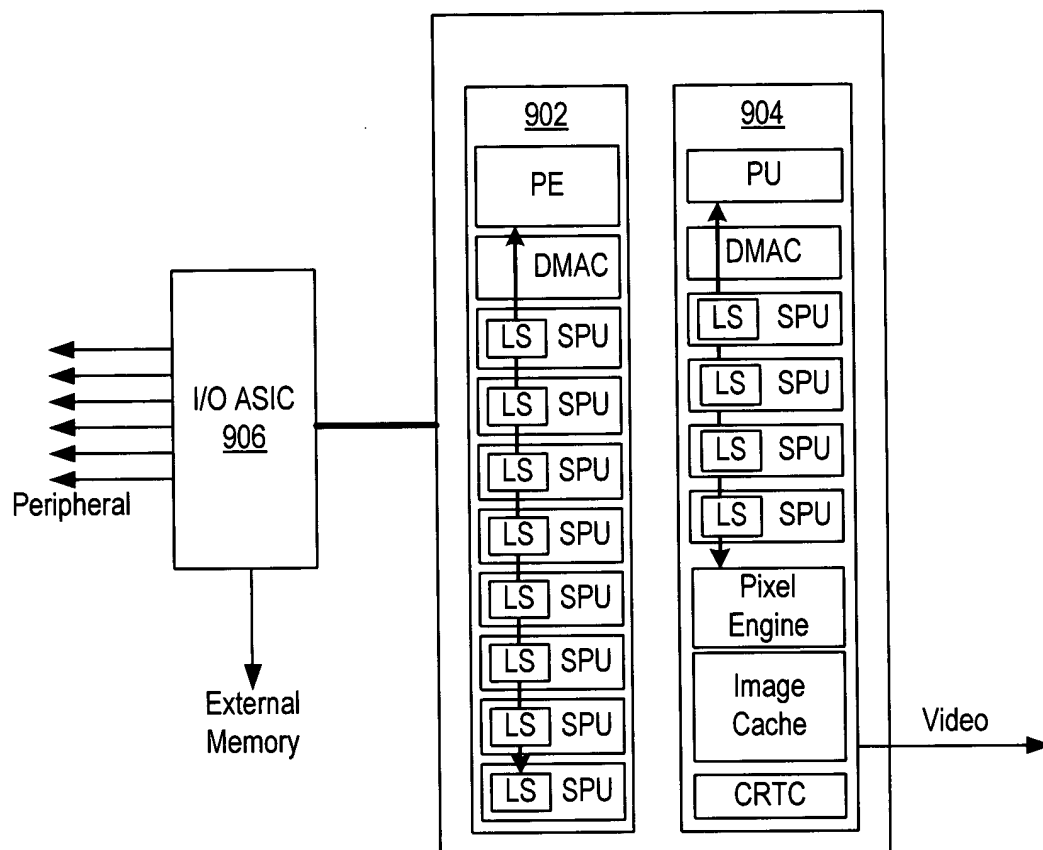
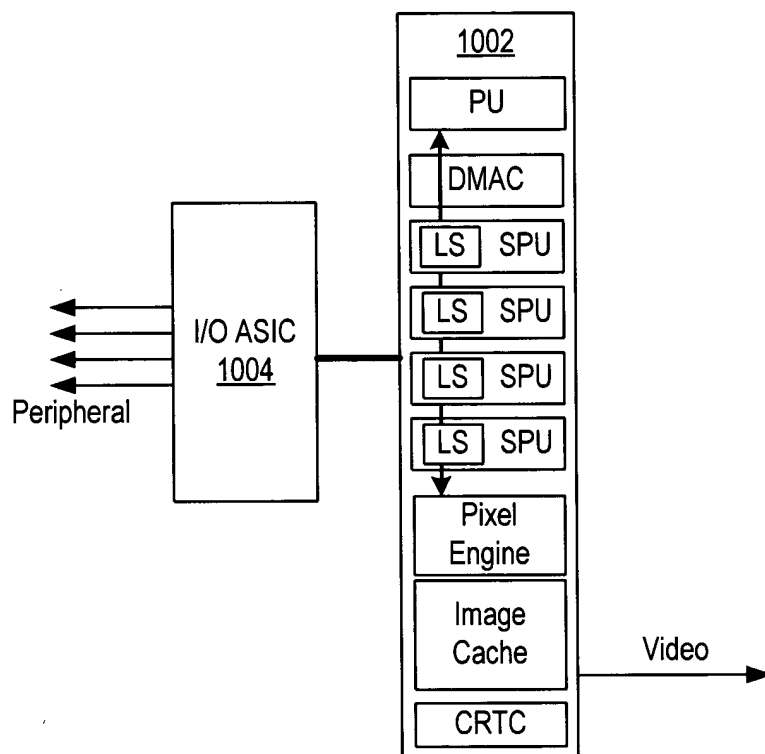


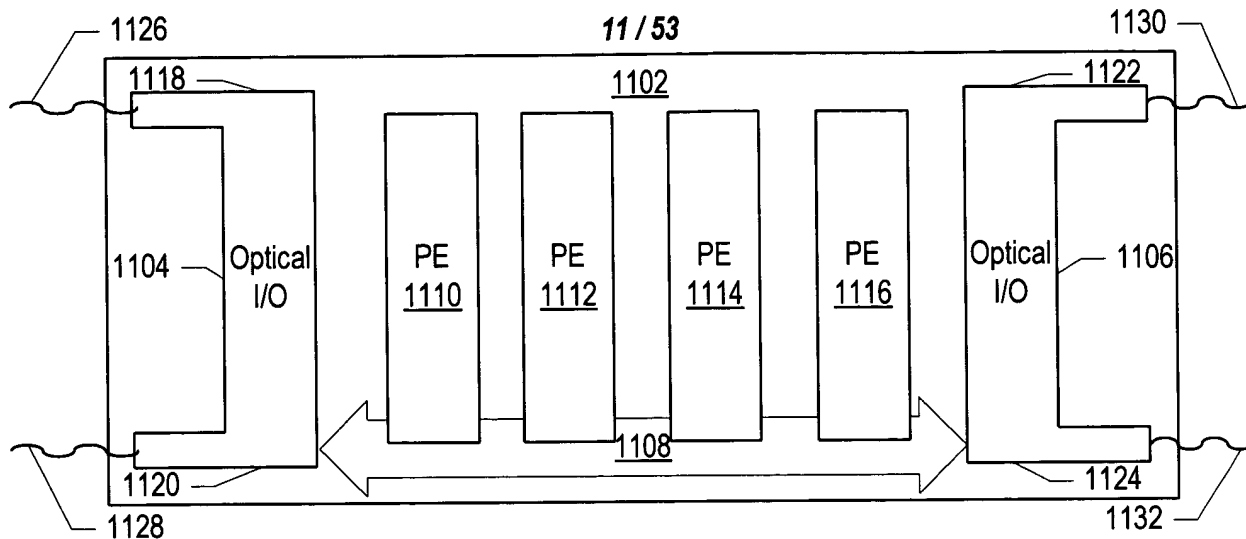
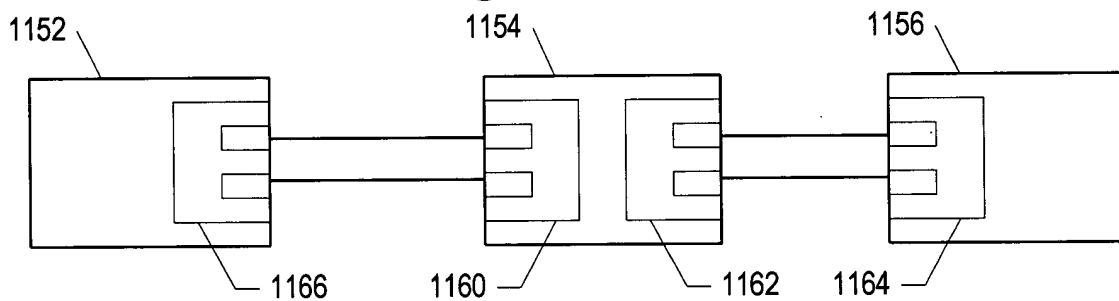
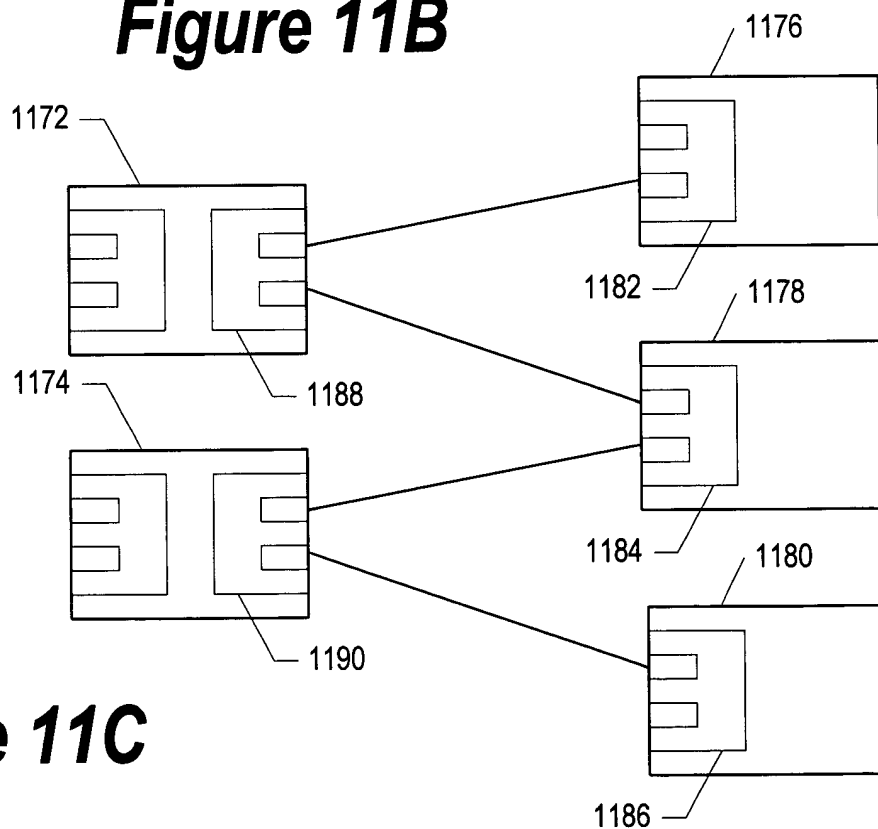
Figure 6

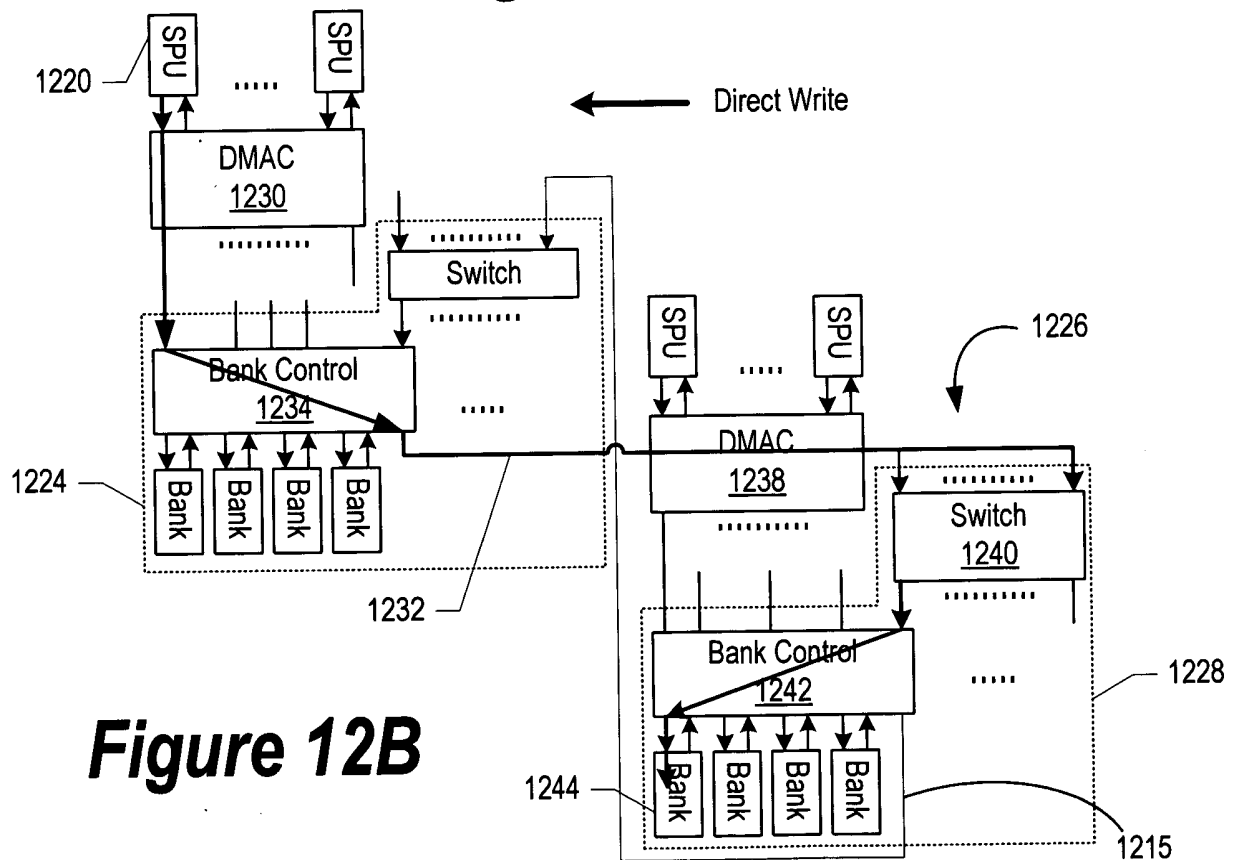
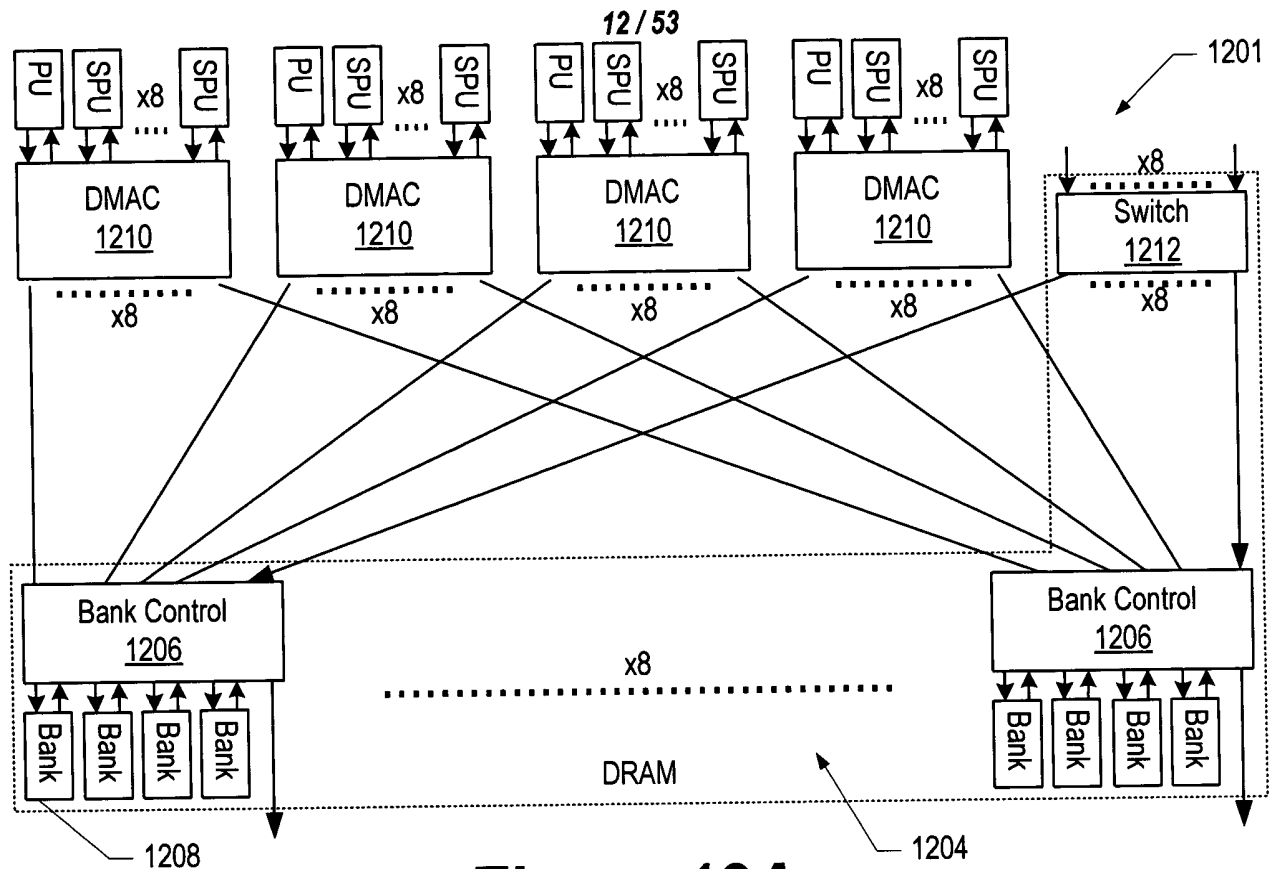
**Figure 7**

**Figure 8**

**Figure 9**

**Figure 10**

**Figure 11A****Figure 11B****Figure 11C**



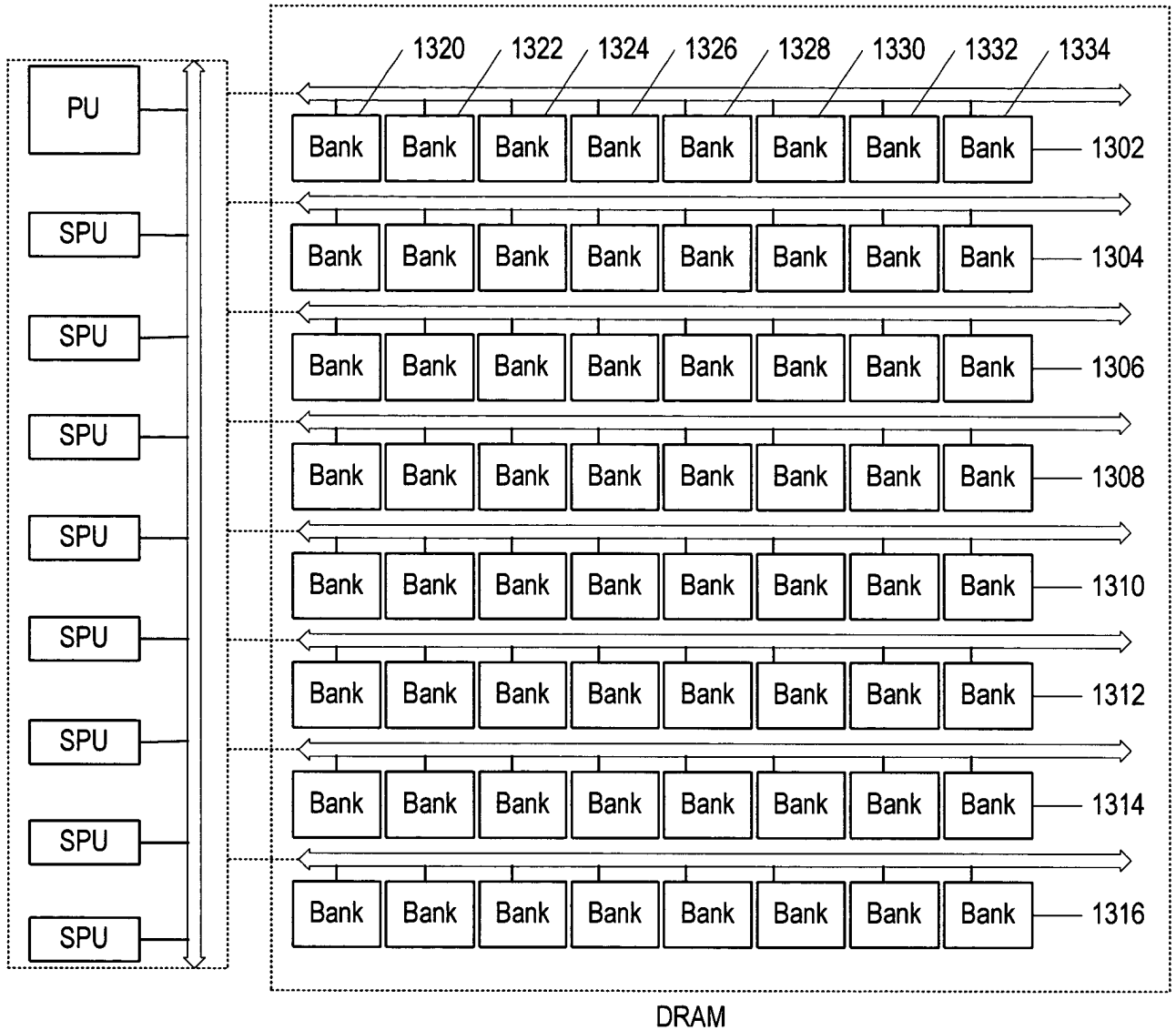


Figure 13

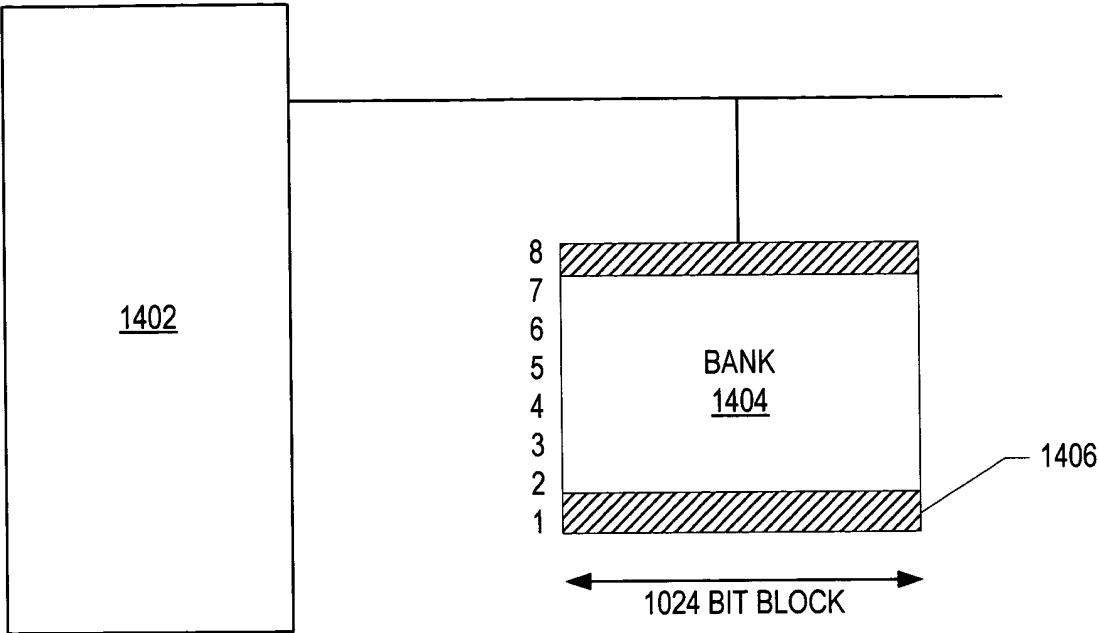


Figure 14A

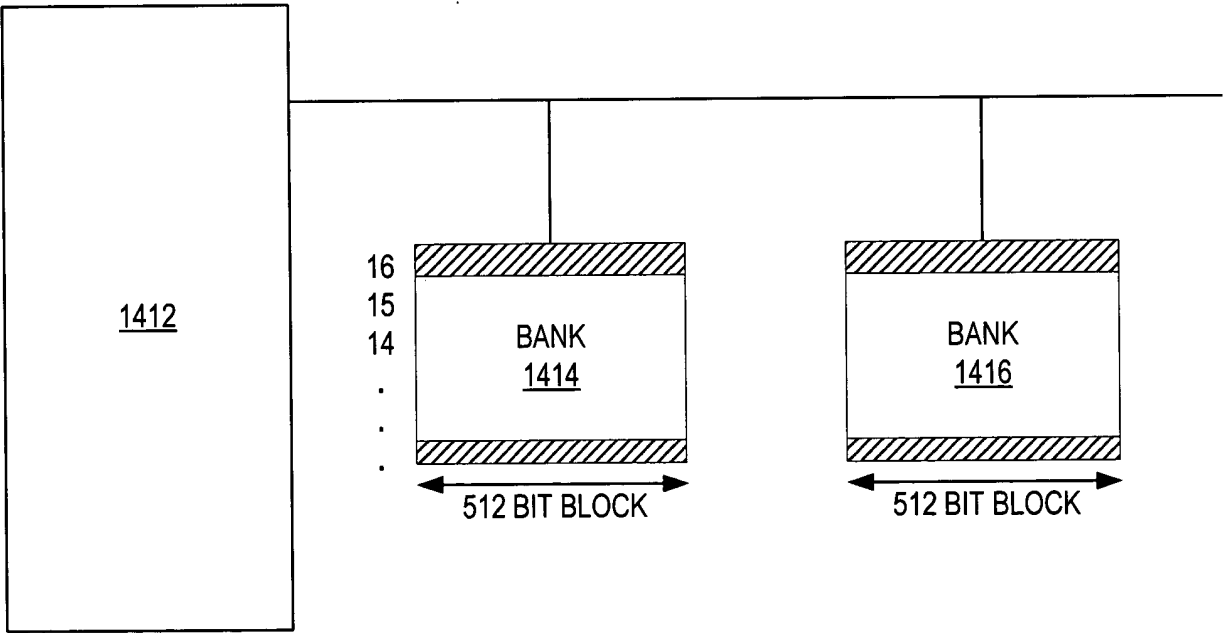
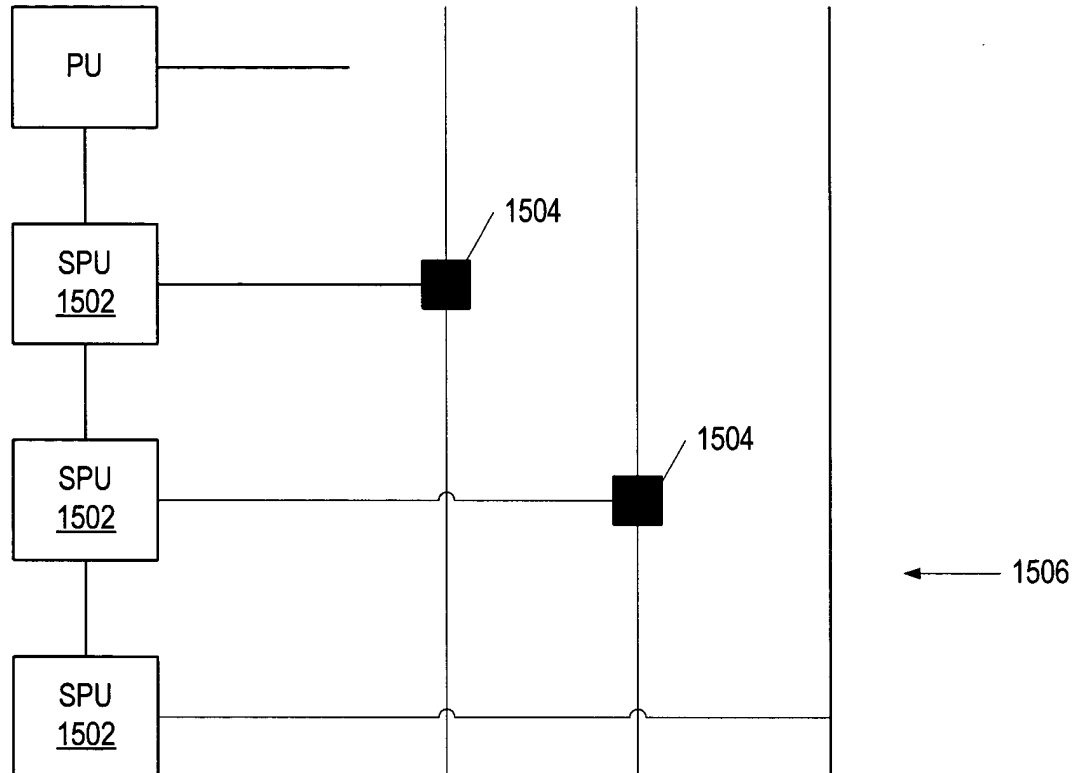
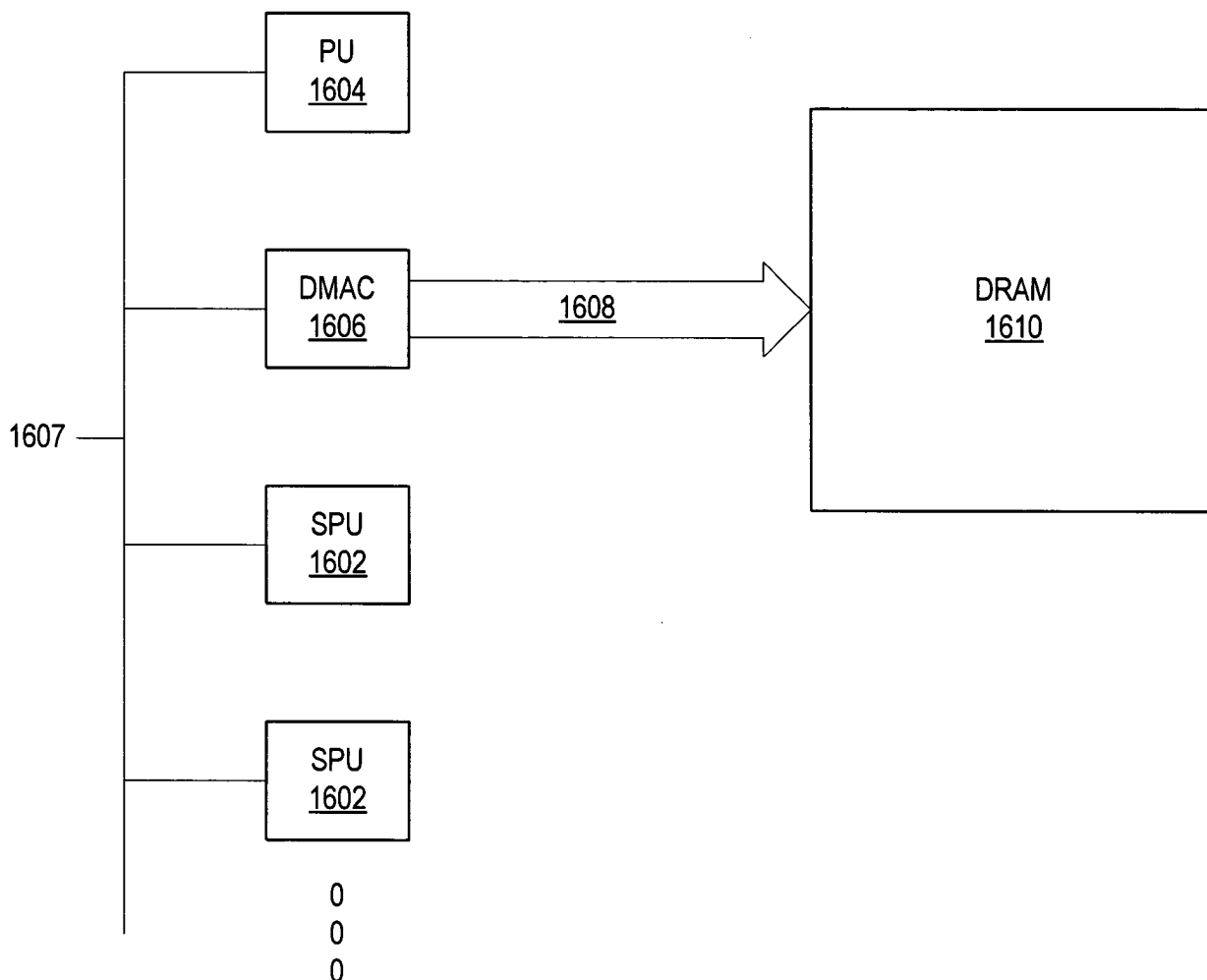


Figure 14B

**Figure 15**

**Figure 16**

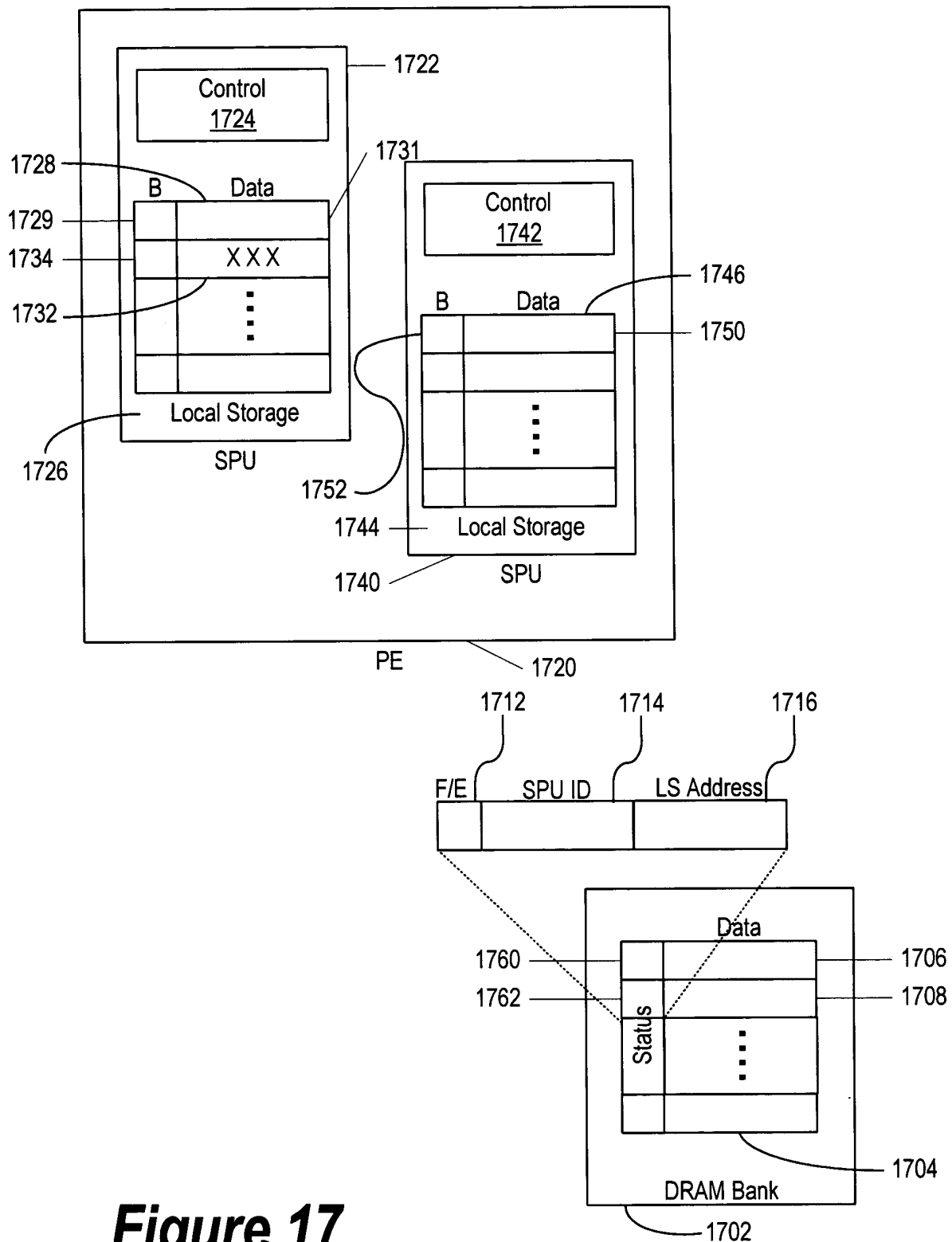


Figure 17

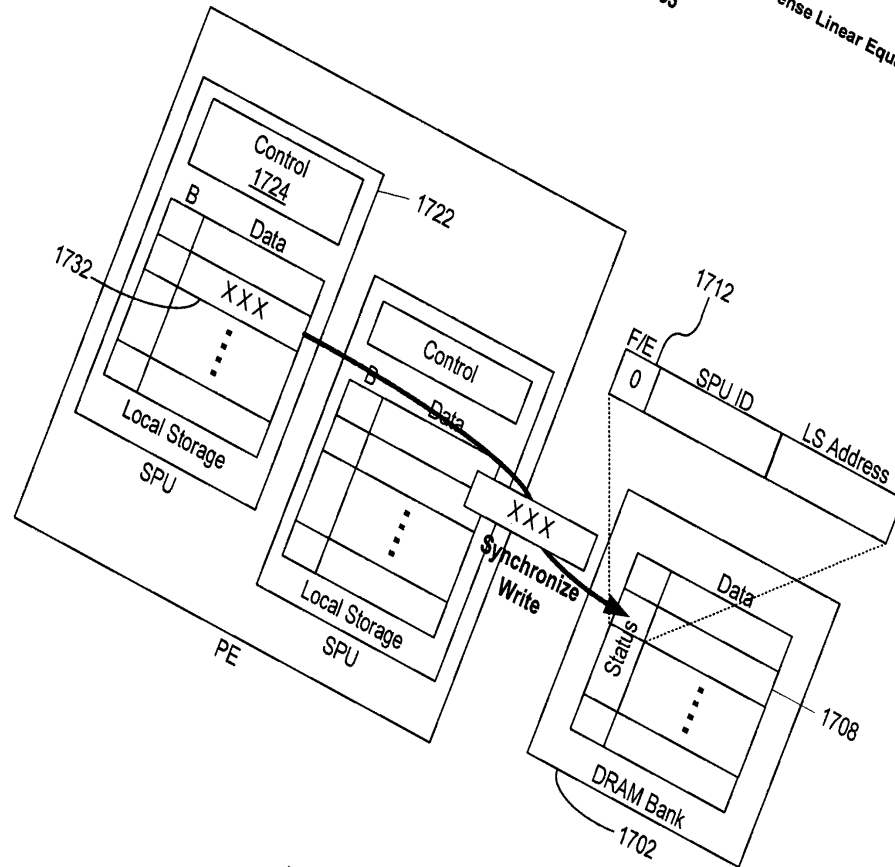


Figure 18

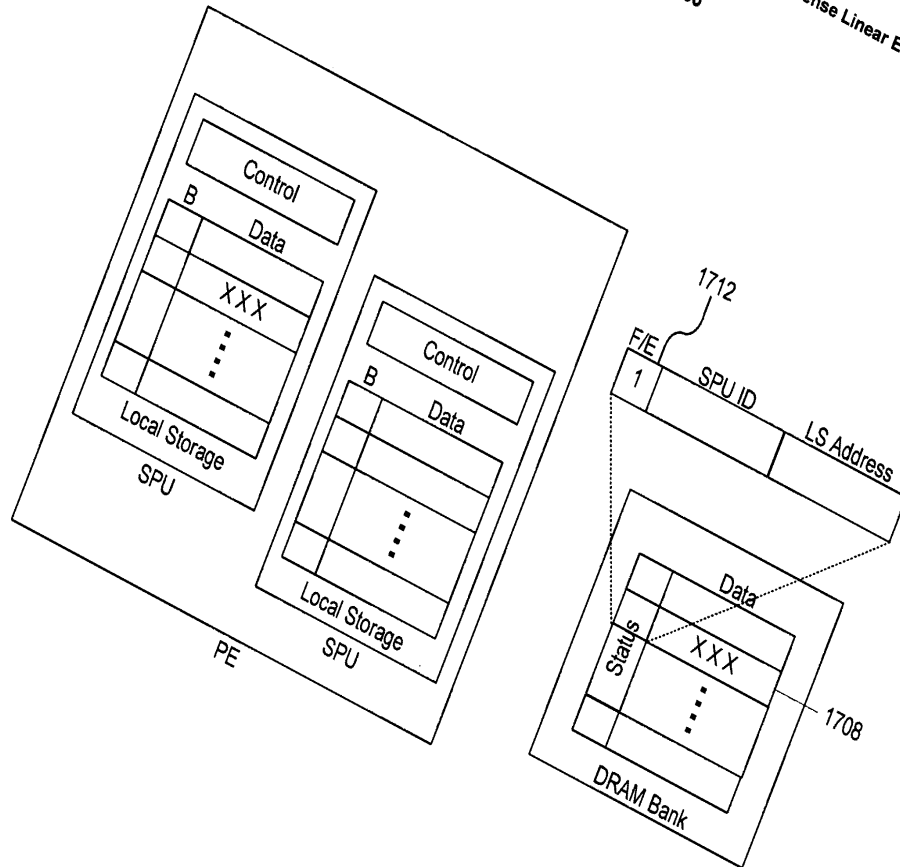
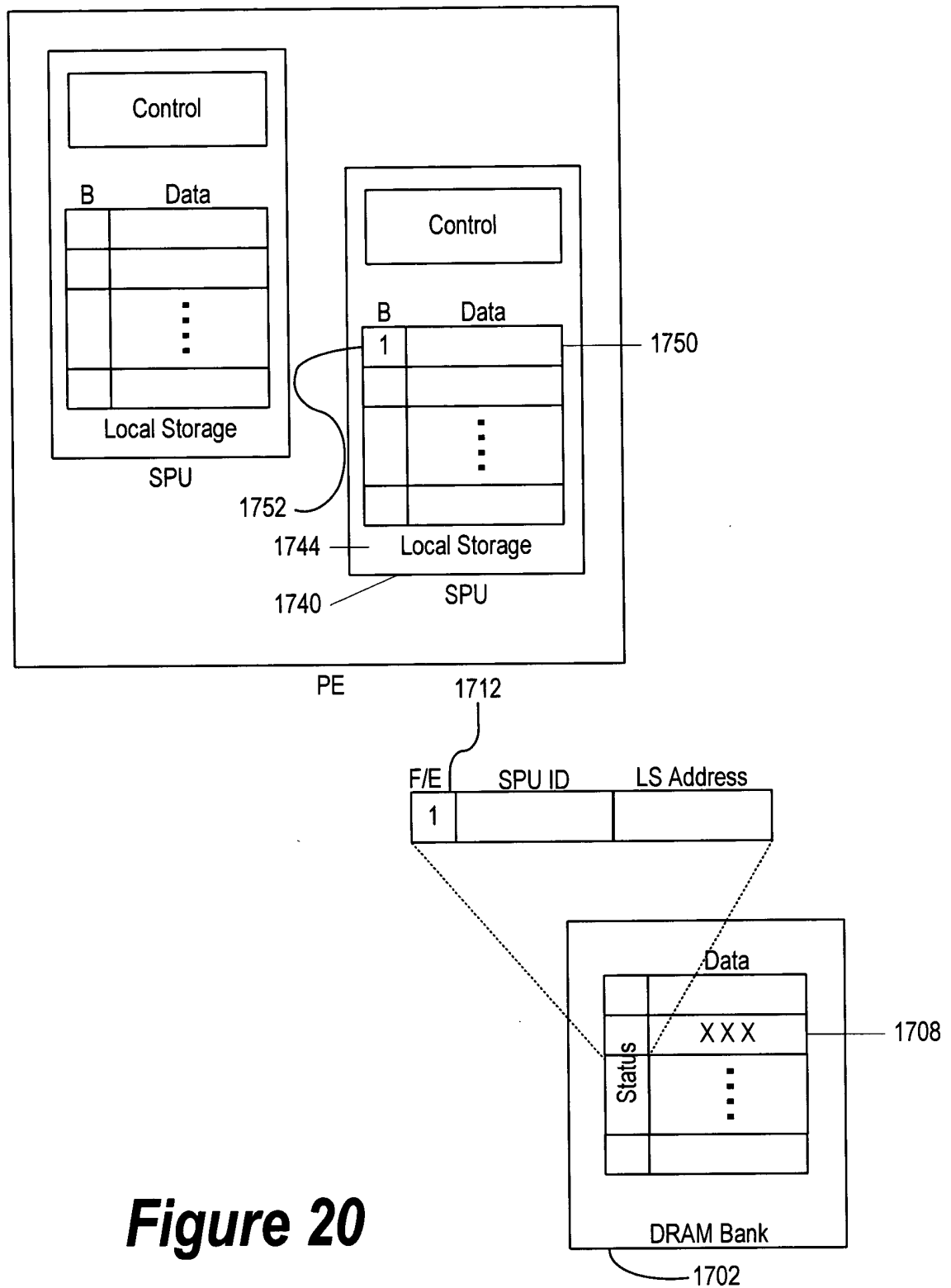


Figure 19

**Figure 20**

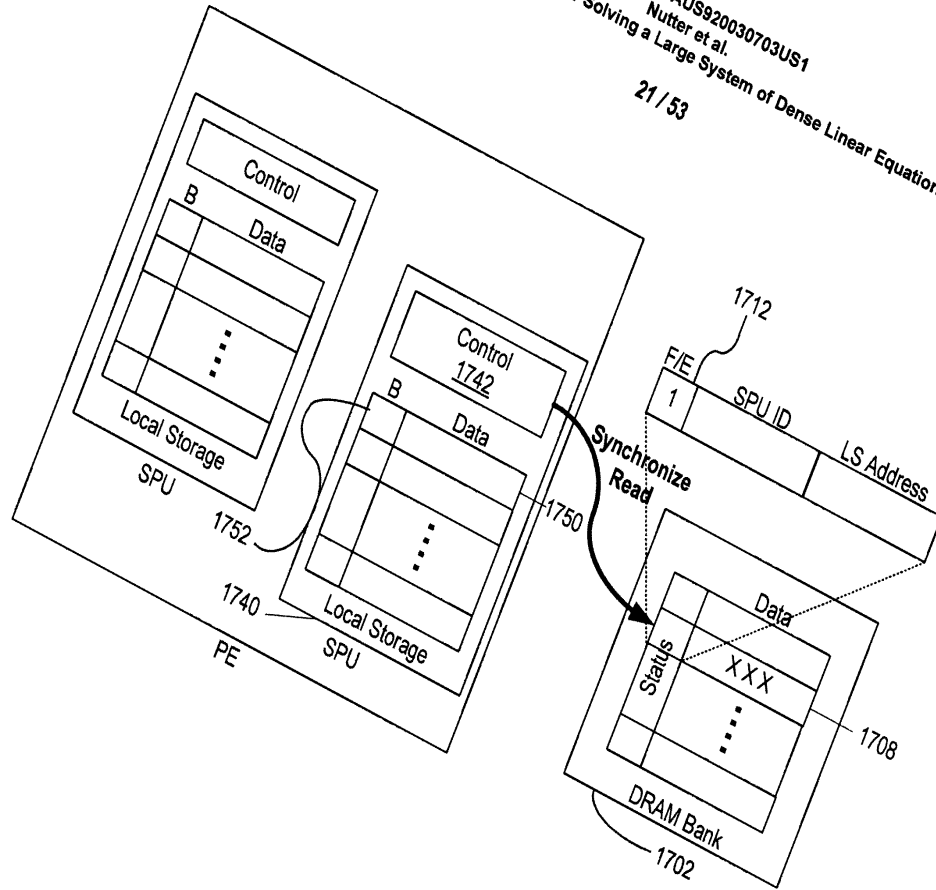
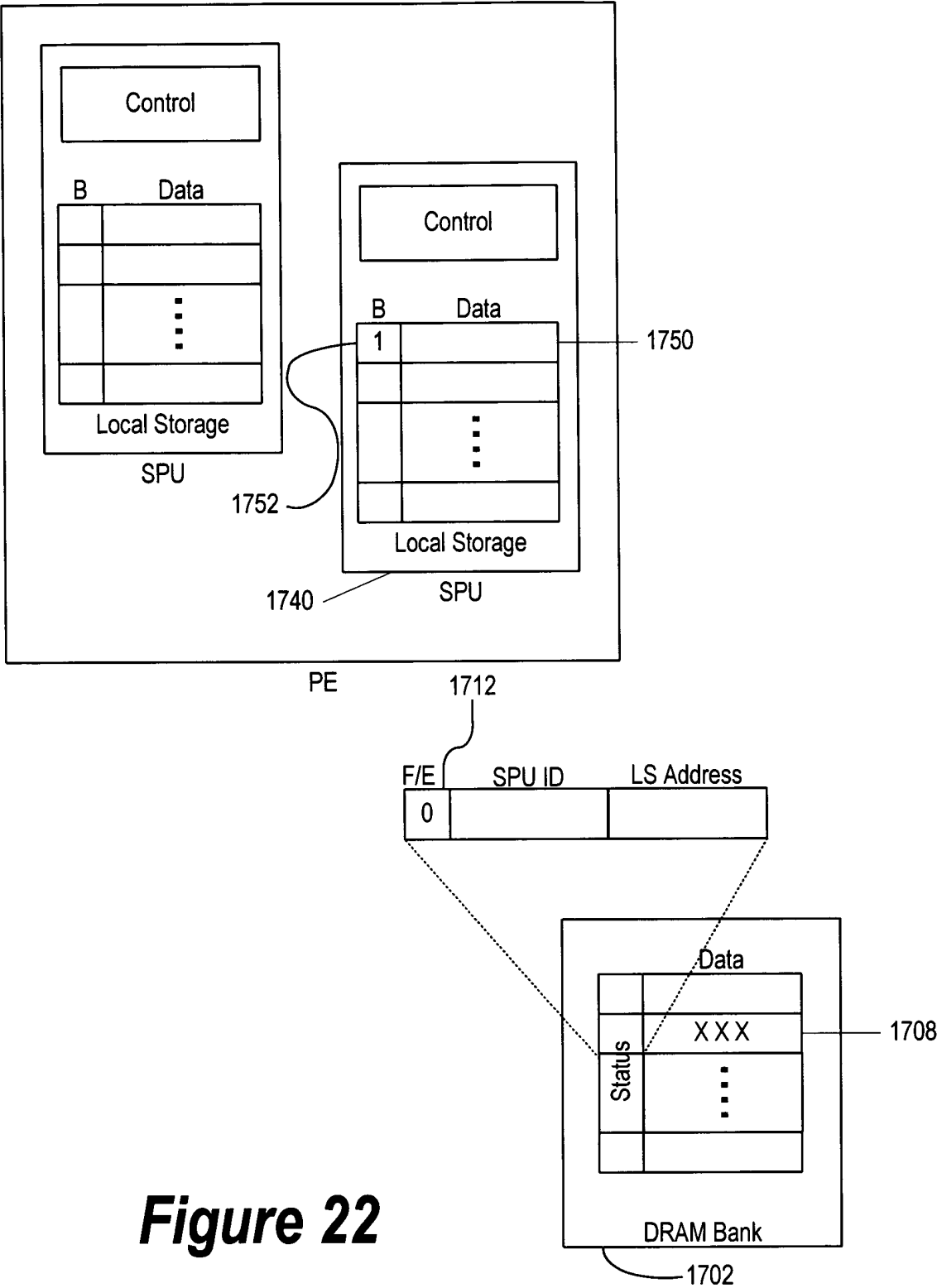


Figure 21



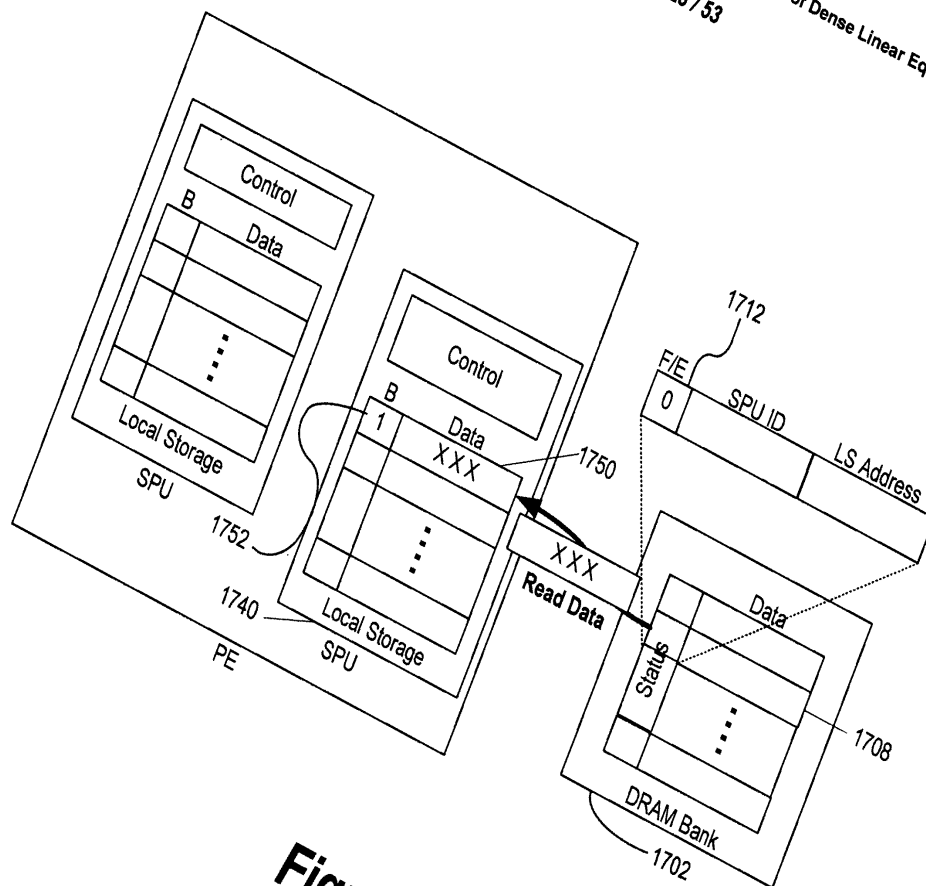
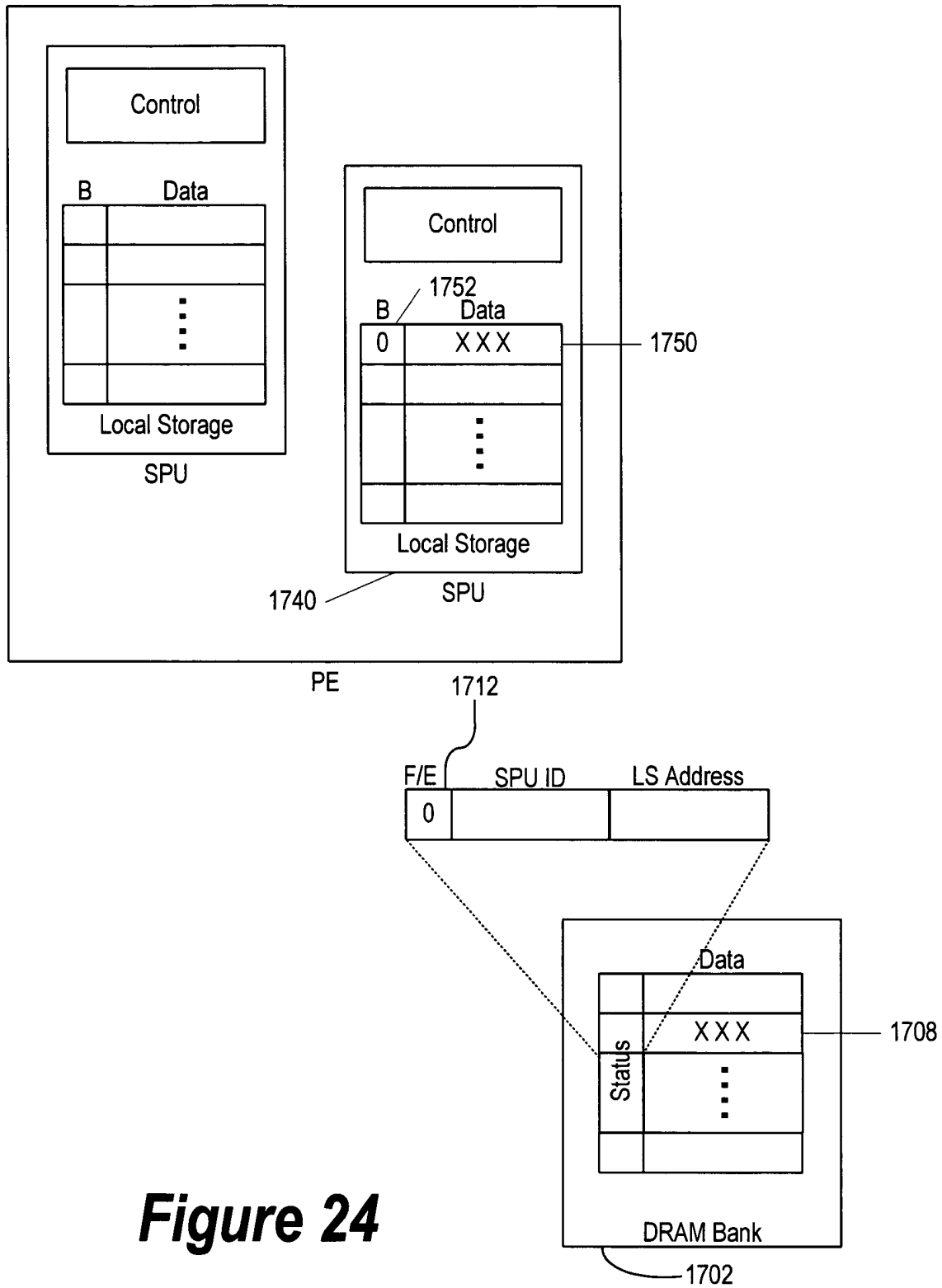


Figure 23

**Figure 24**

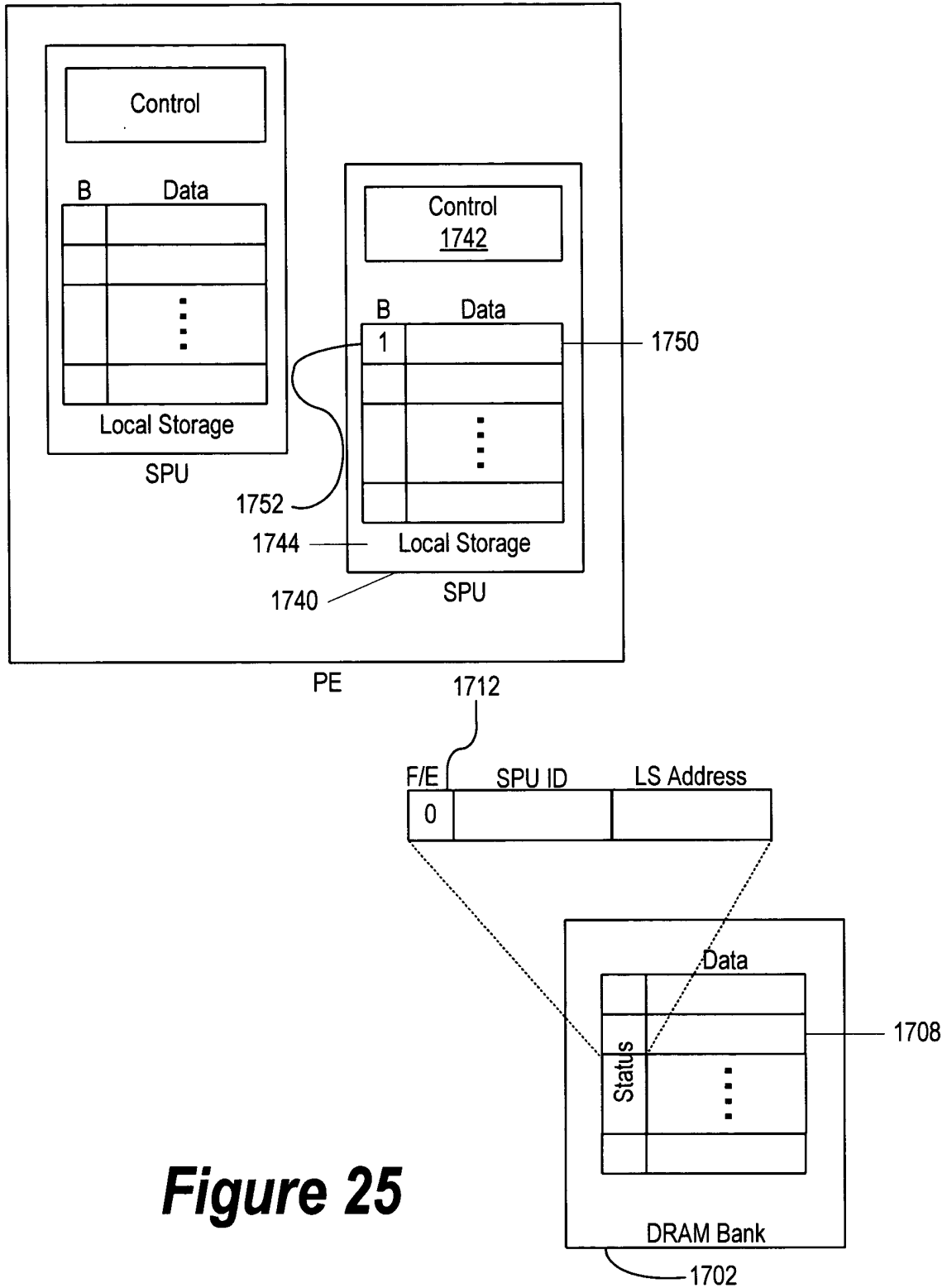


Figure 25

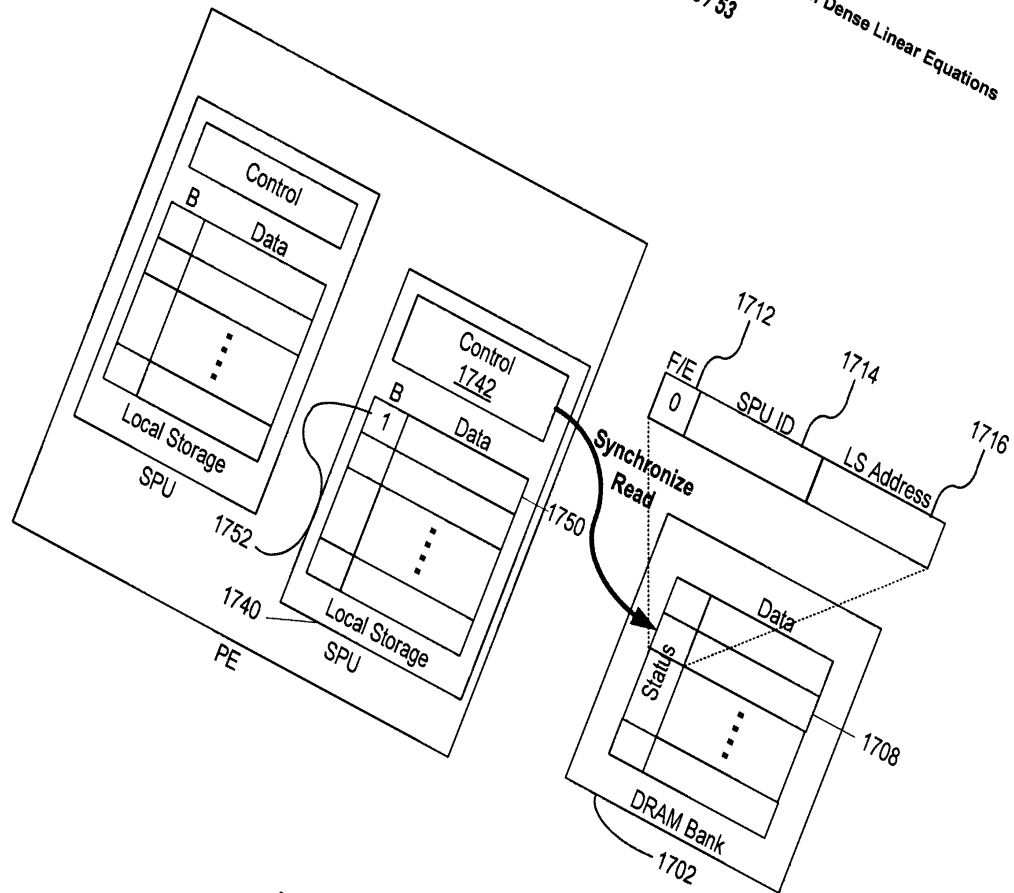


Figure 26

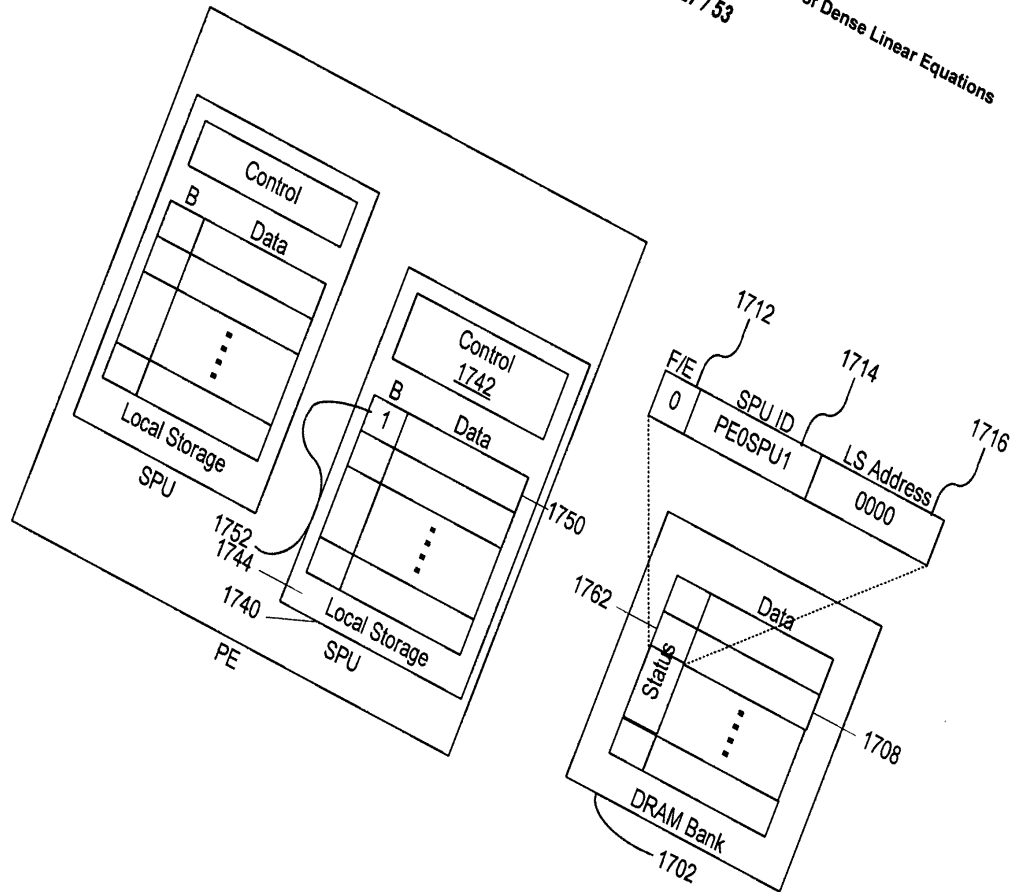


Figure 27

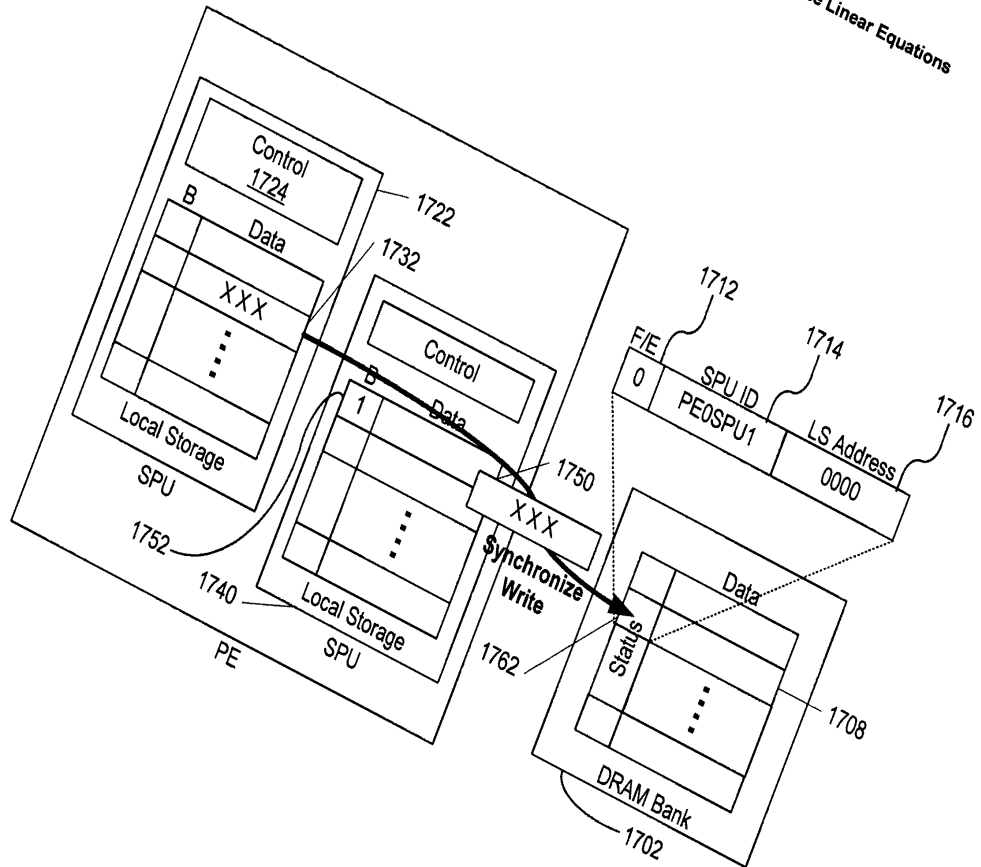


Figure 28

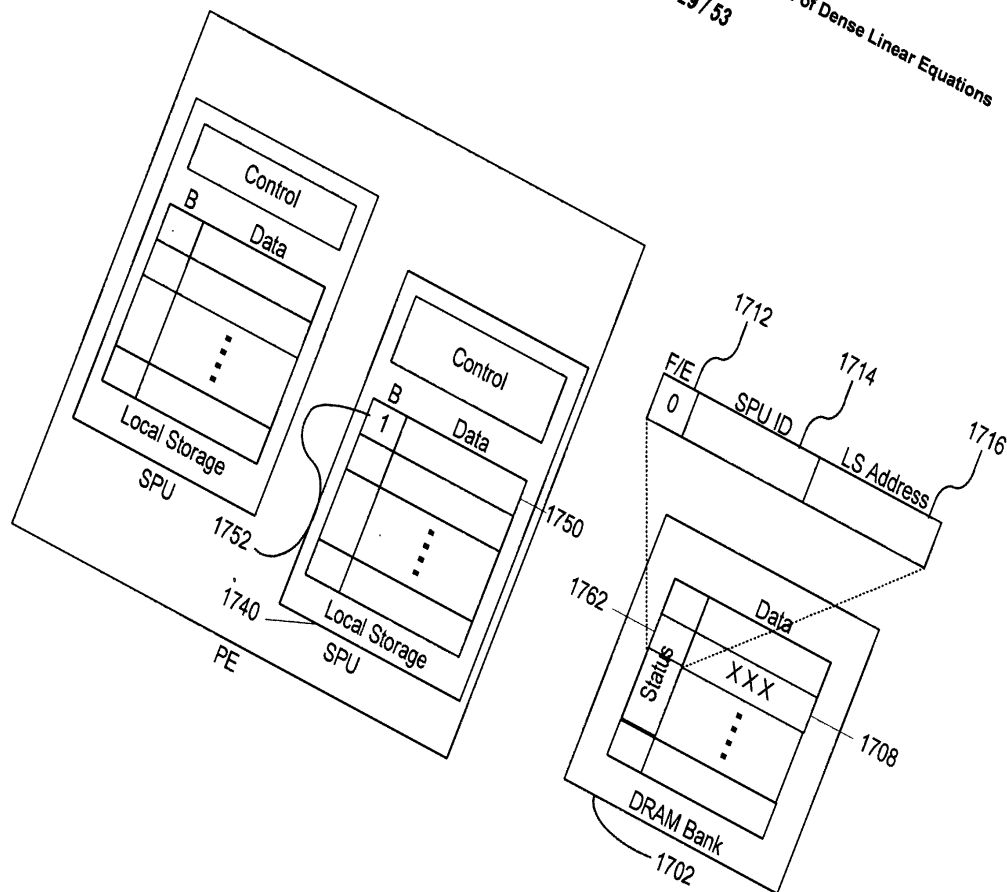


Figure 29

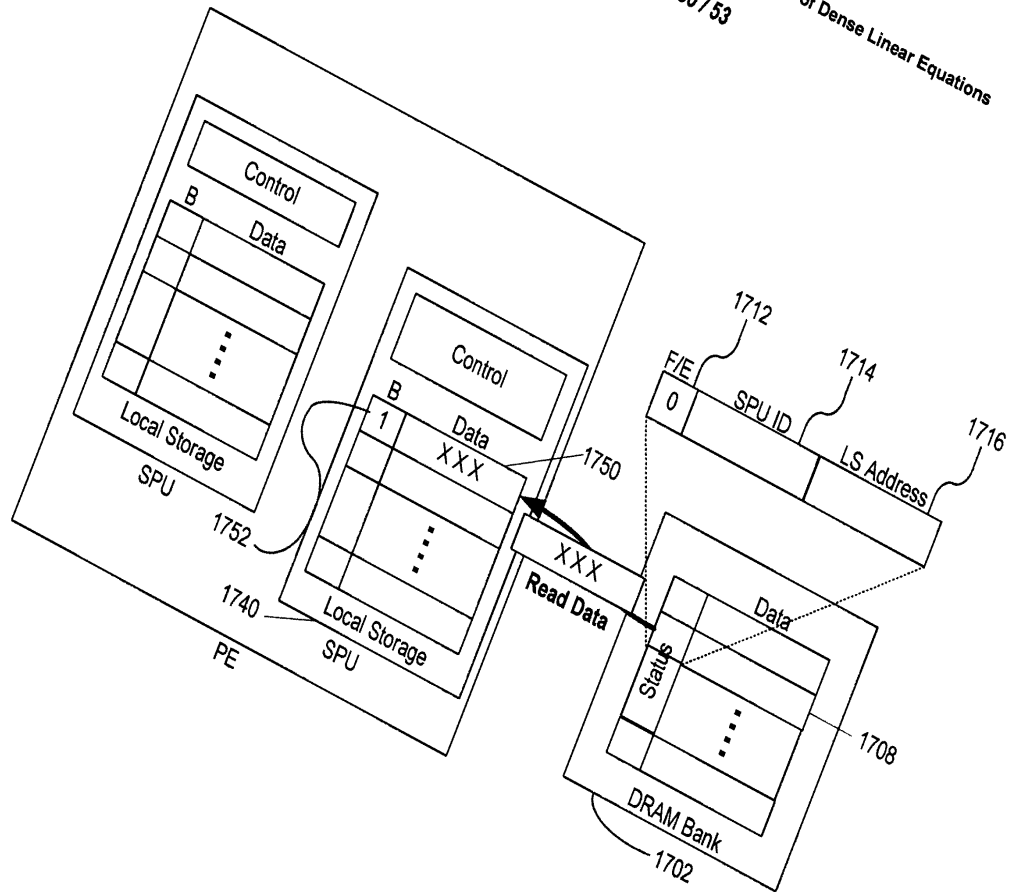


Figure 30

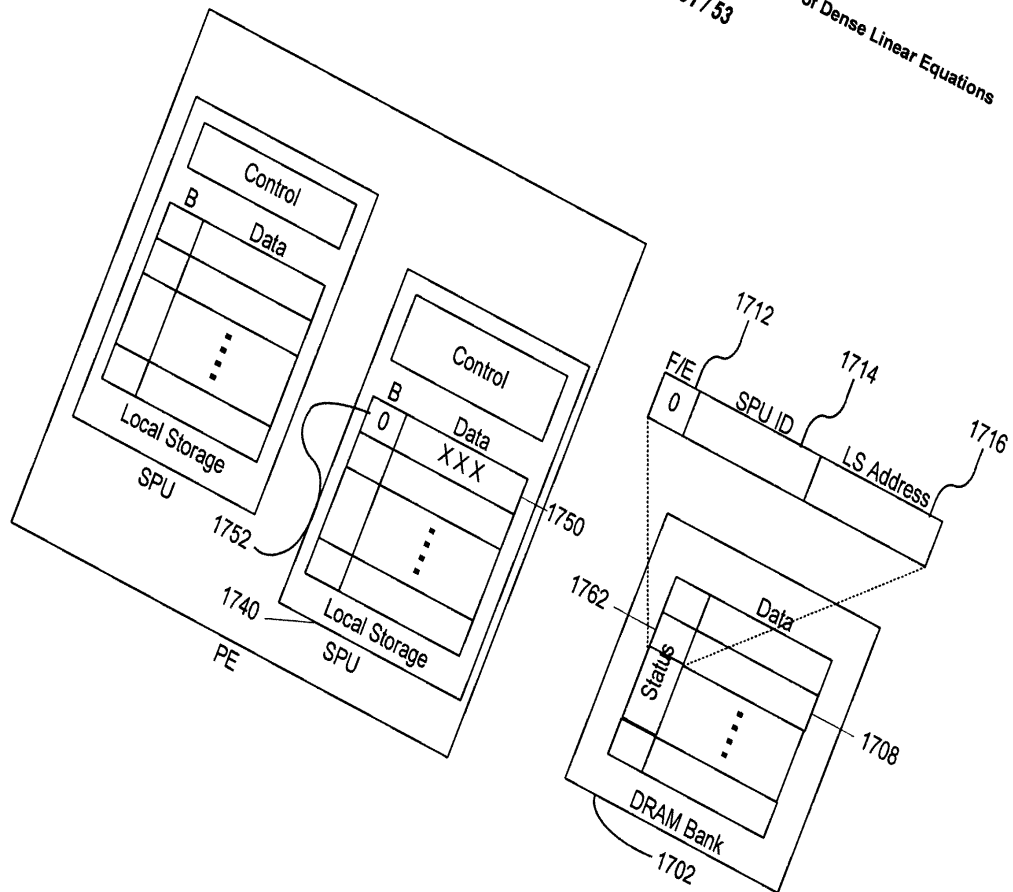
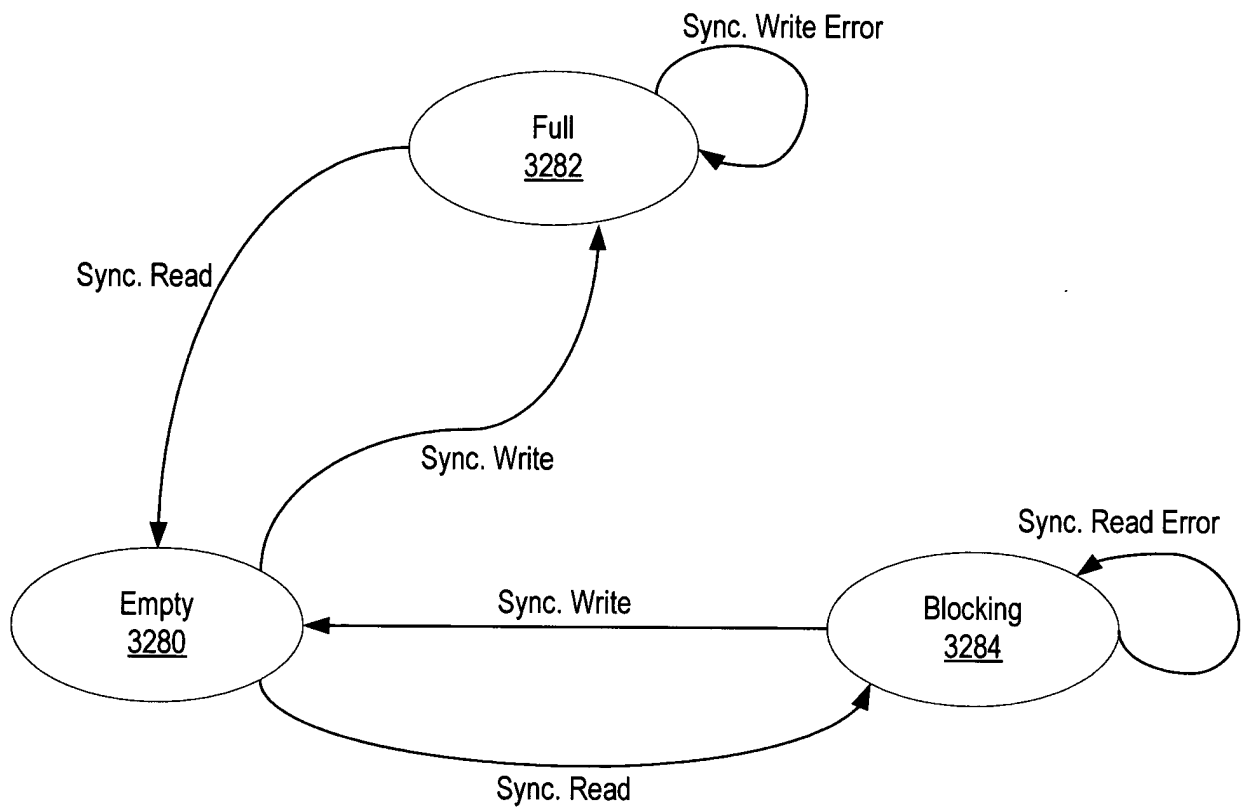


Figure 31

**Figure 32**

Key Control Table

3302

3304

ID

3306

3308

0	SPU Key	Key Mask
1	SPU Key	Key Mask
2	SPU Key	Key Mask
		⋮
7	SPU Key	Key Mask

Figure 33

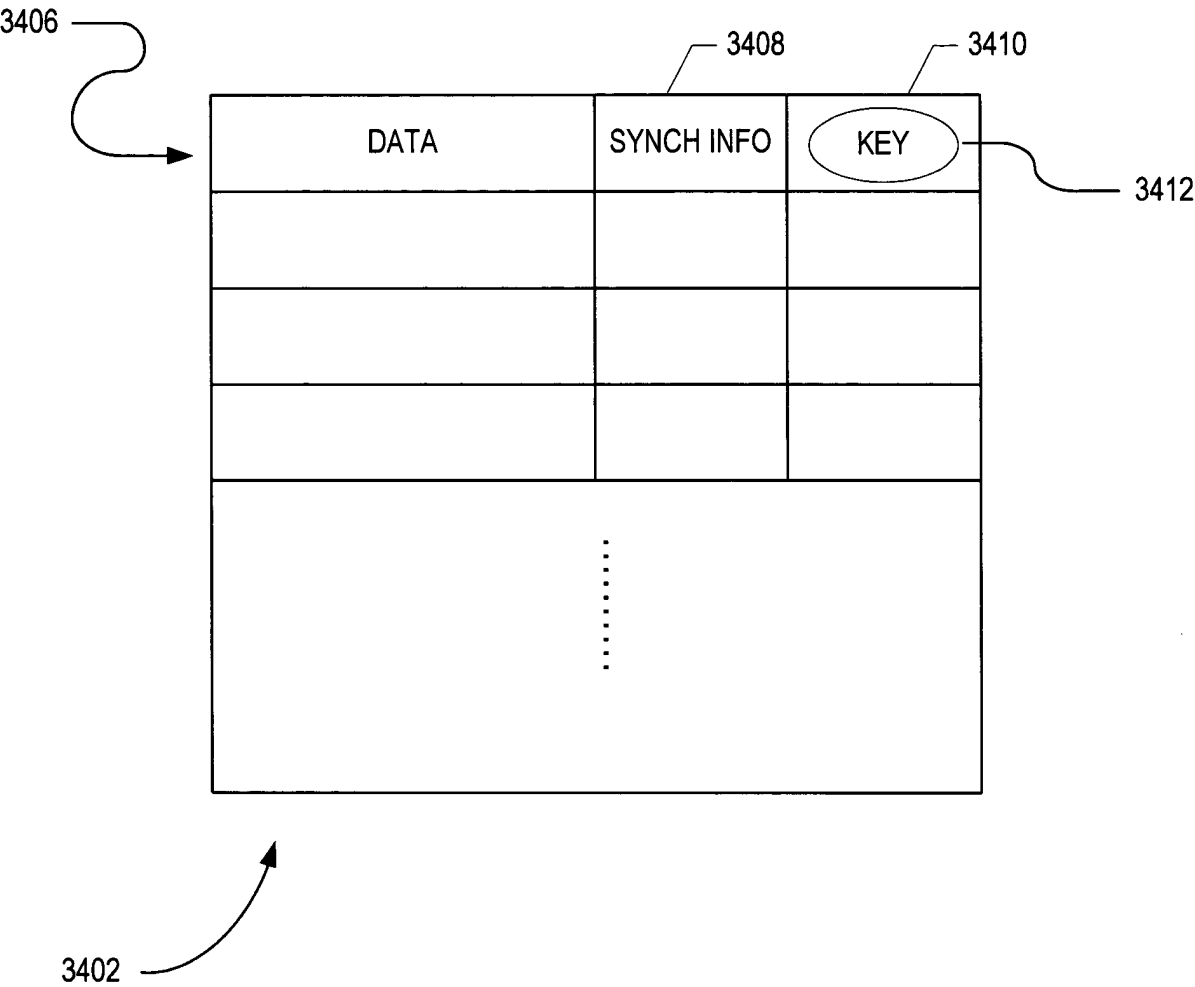


Figure 34

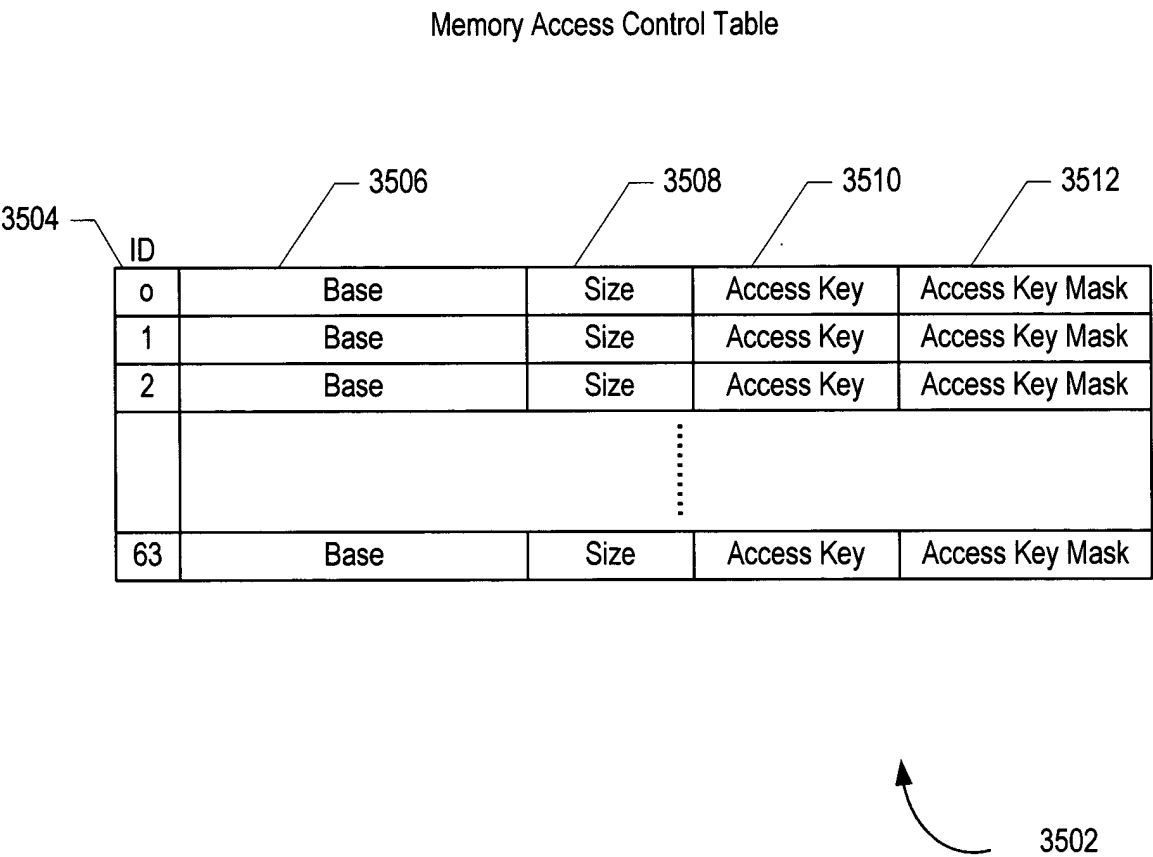


Figure 35

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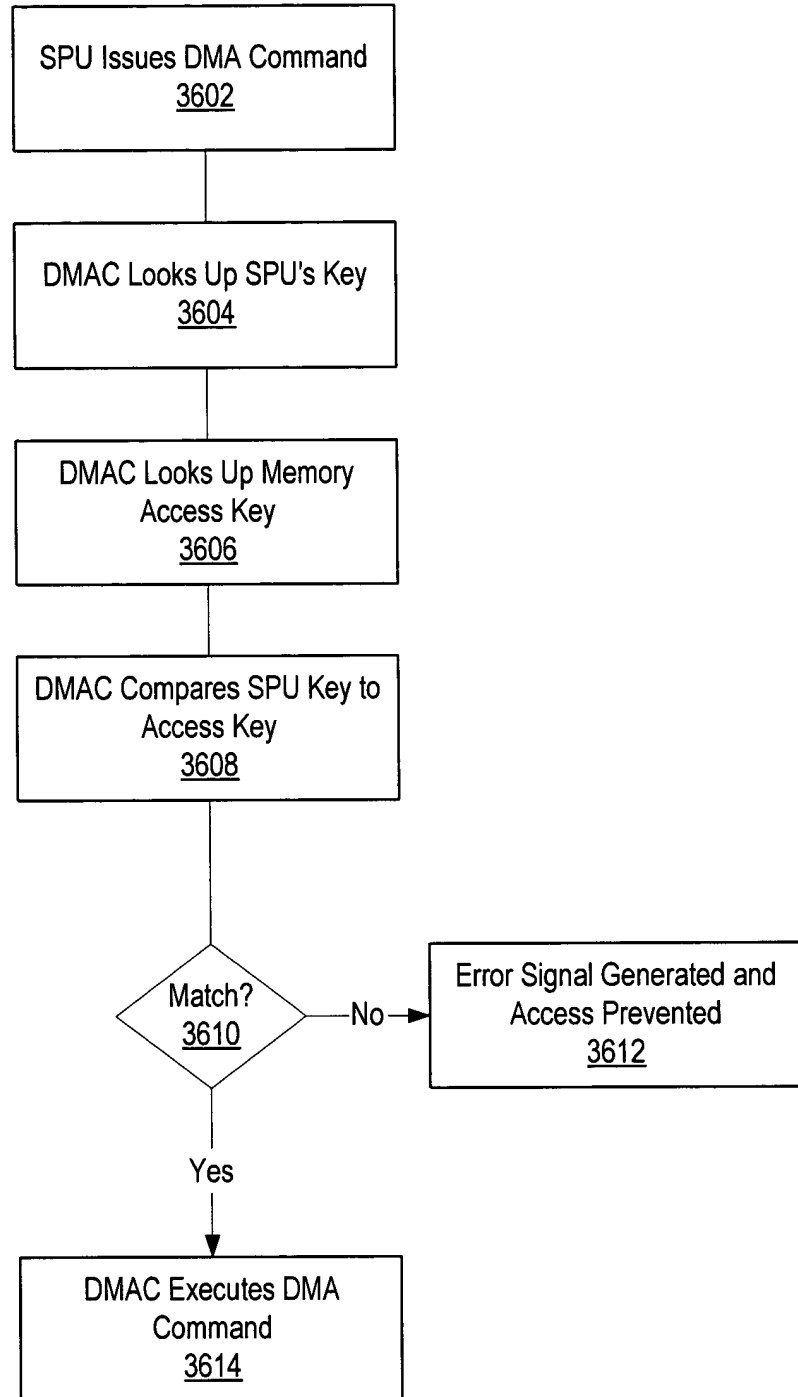
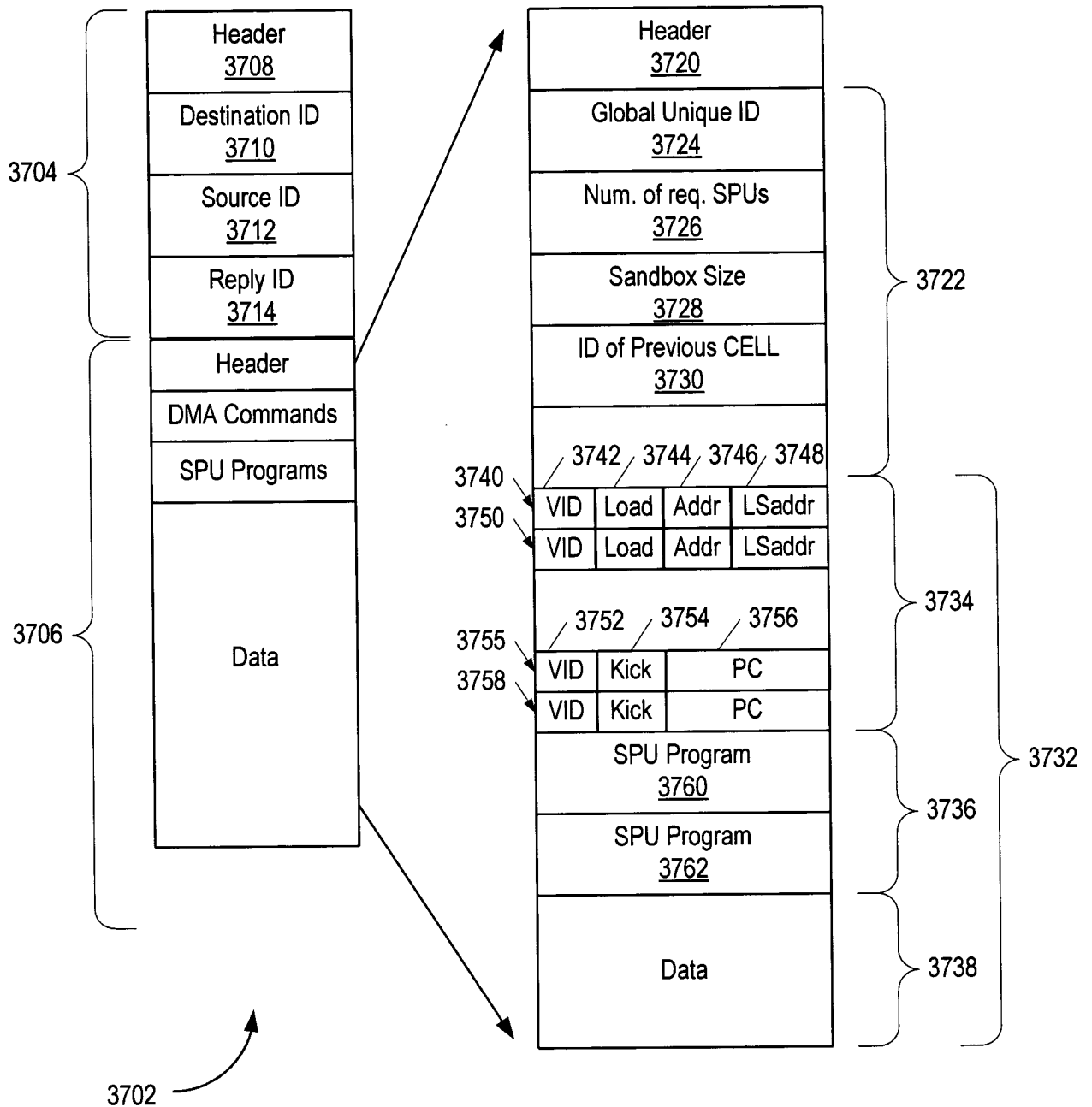
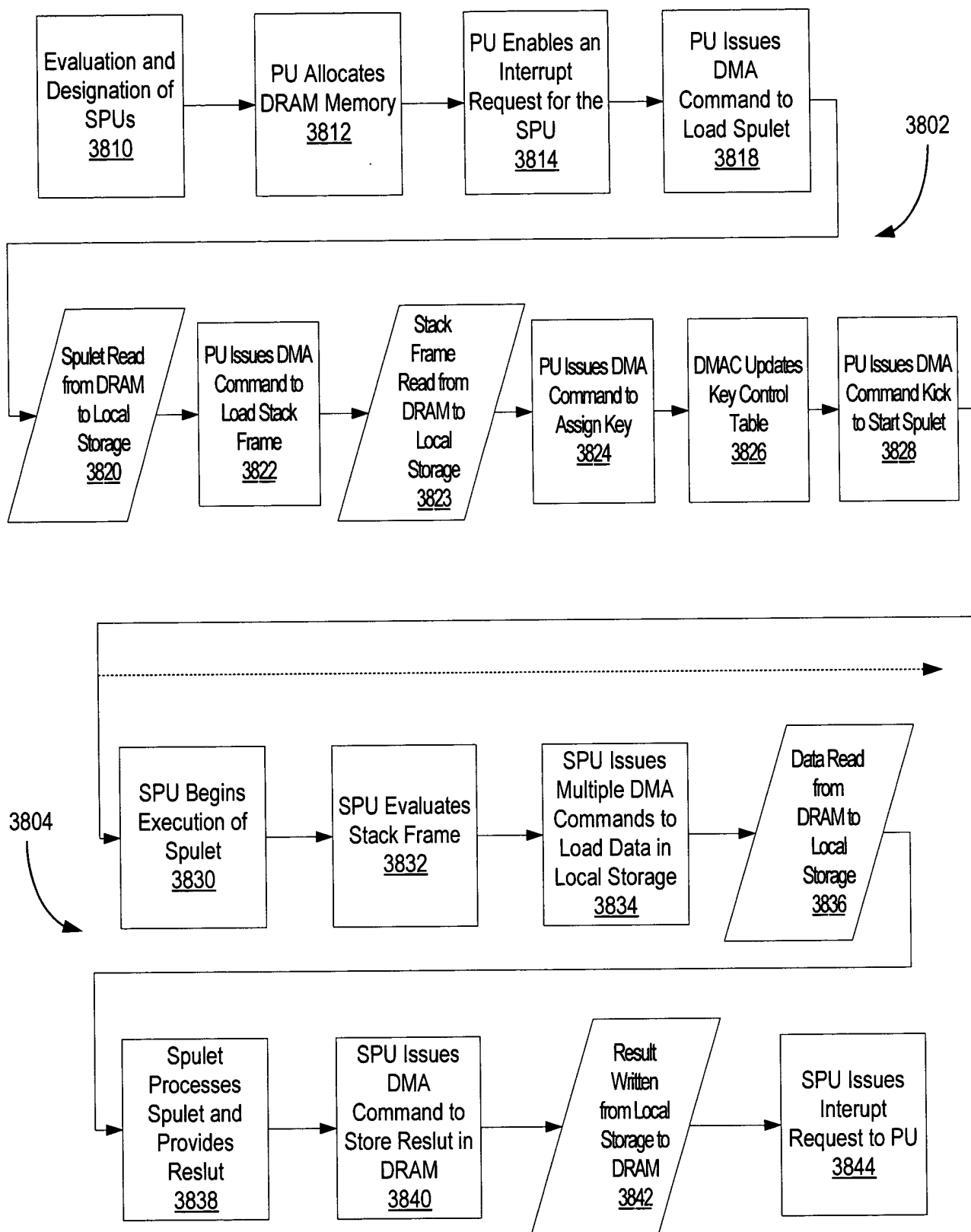
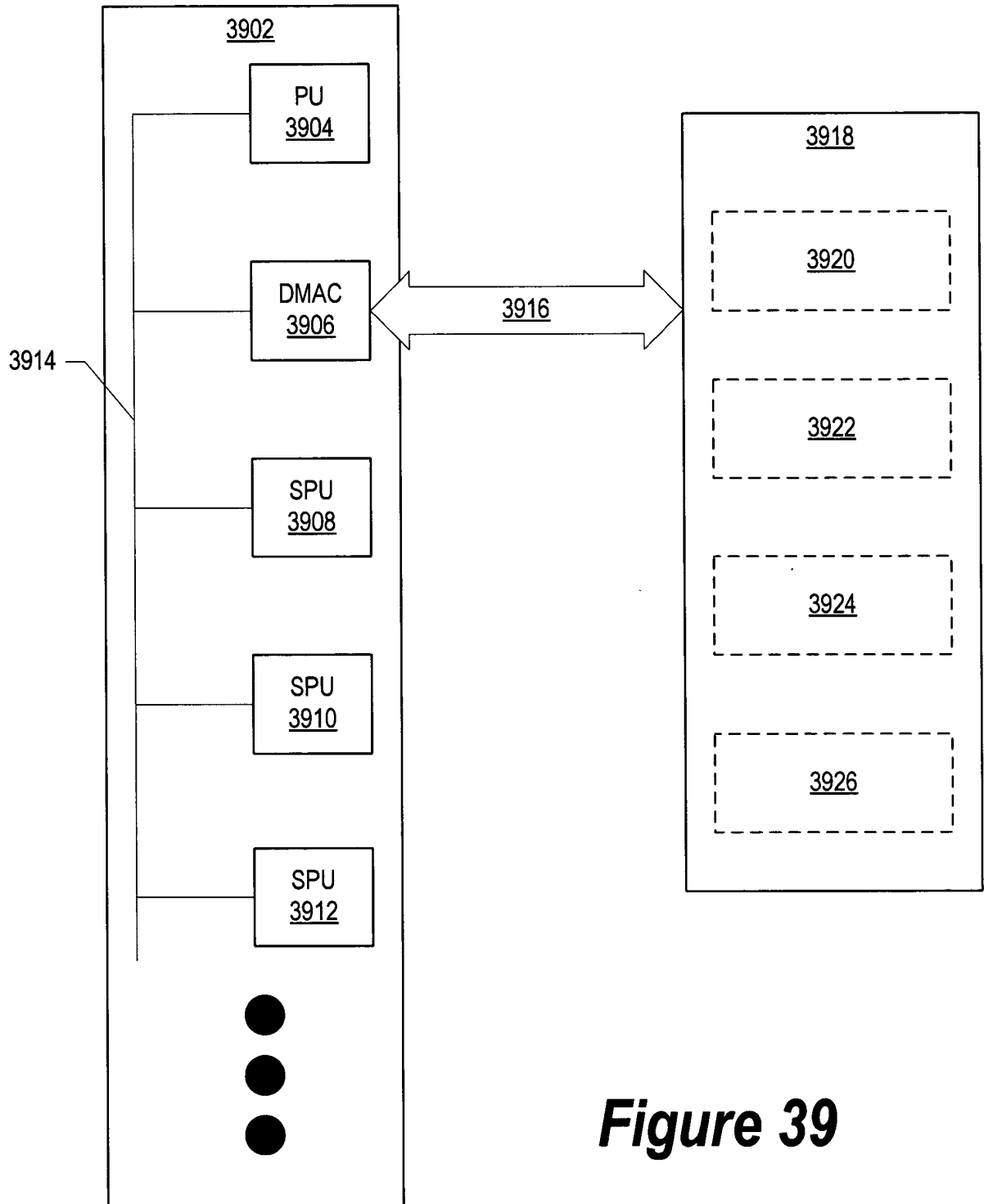


Figure 36

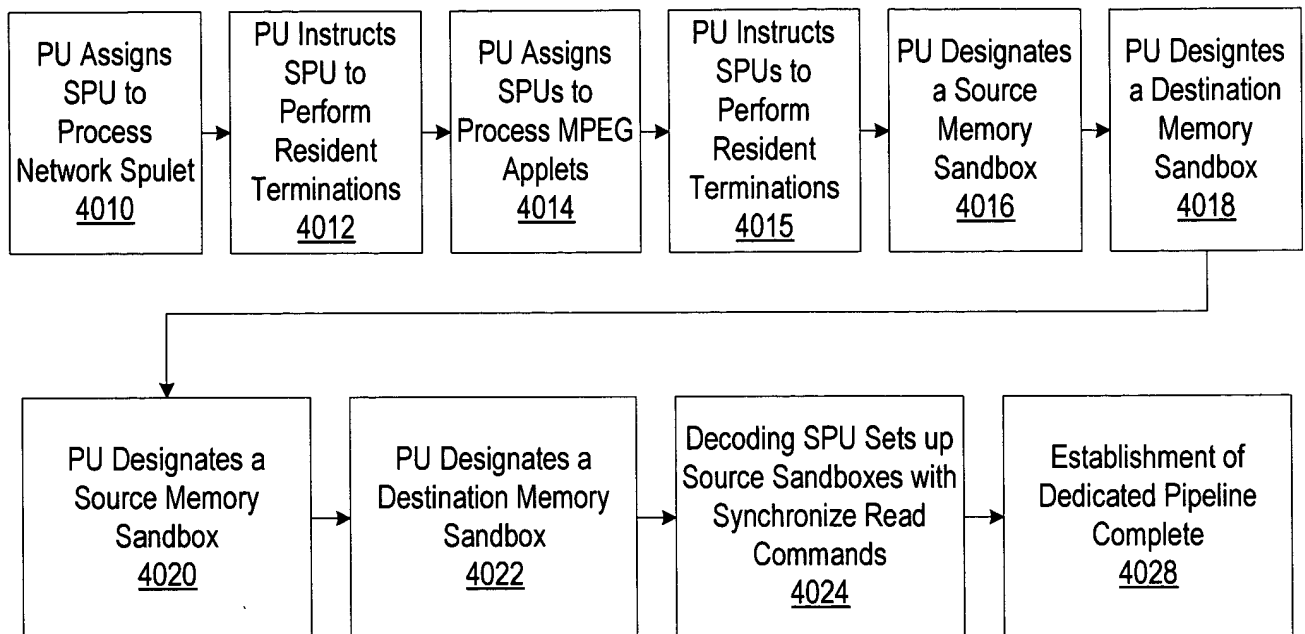
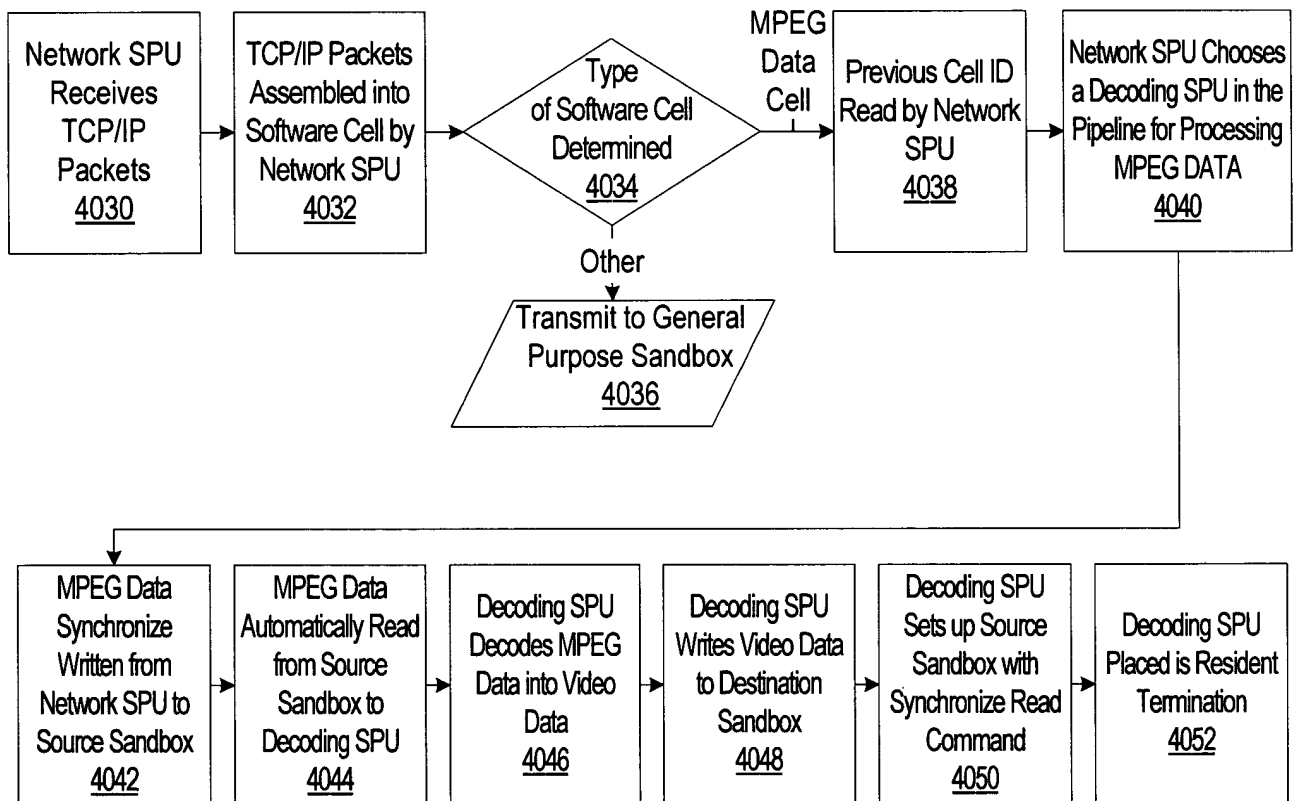
**Figure 37**

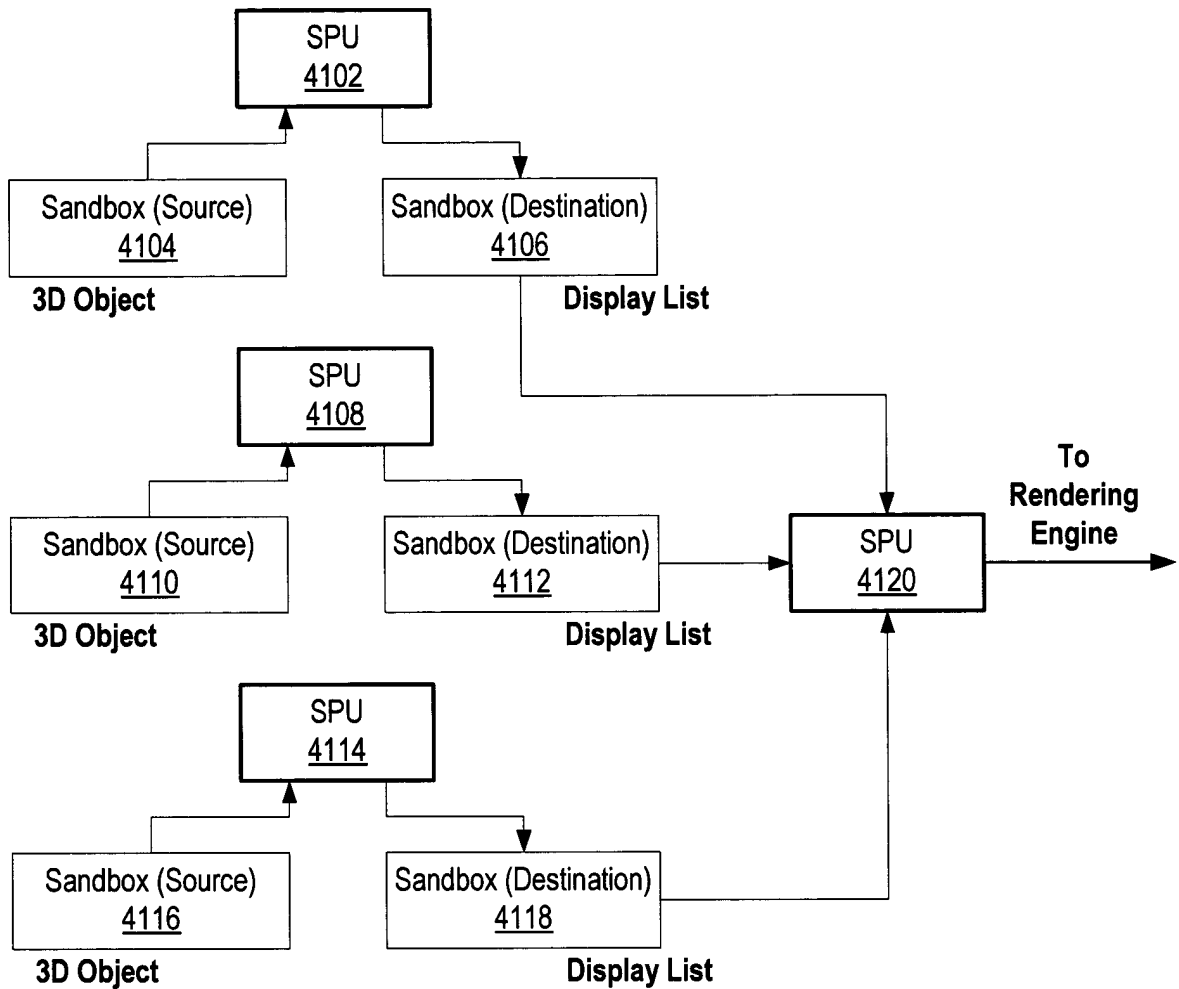
38 / 53

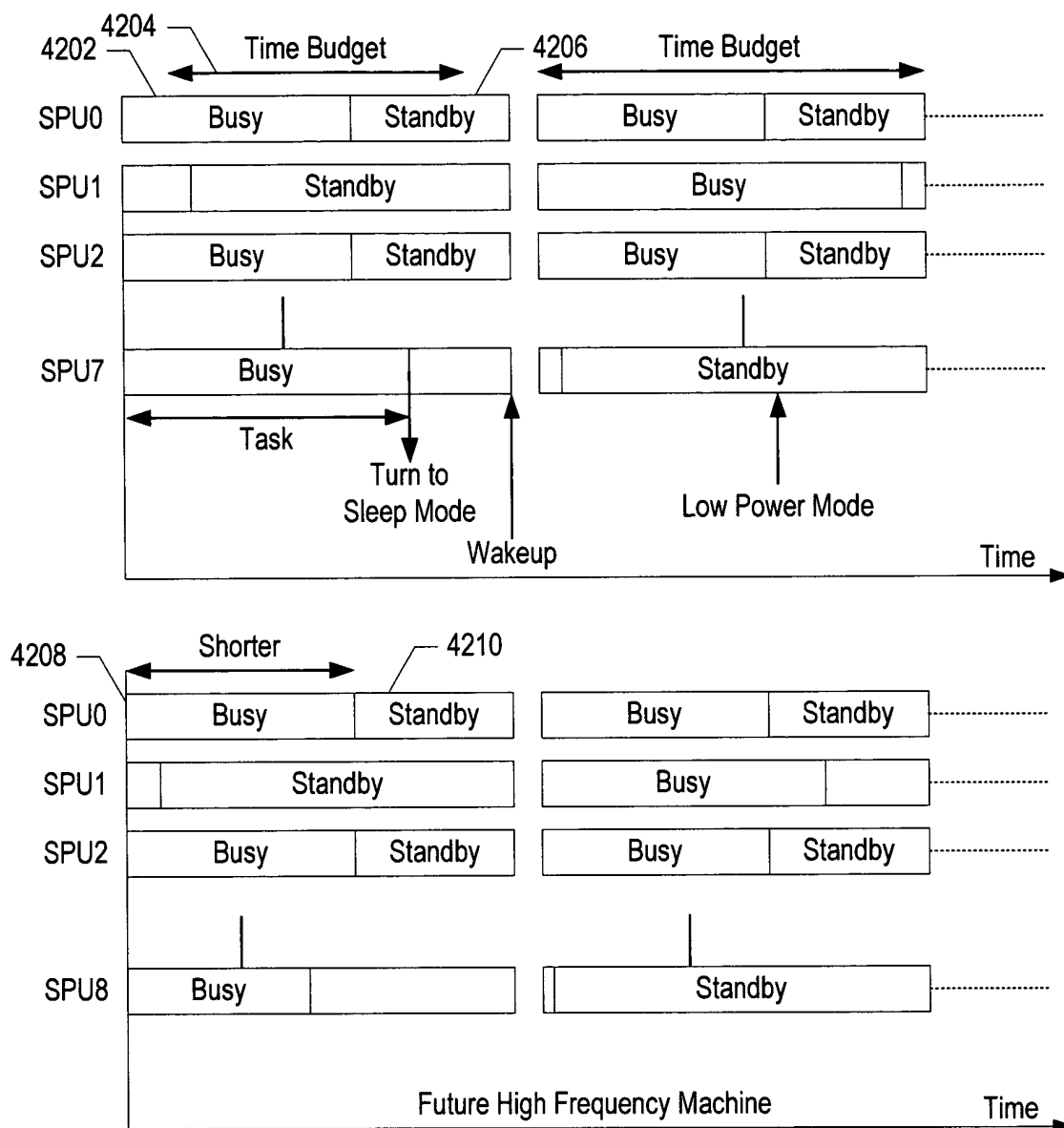
**Figure 38**

**Figure 39**

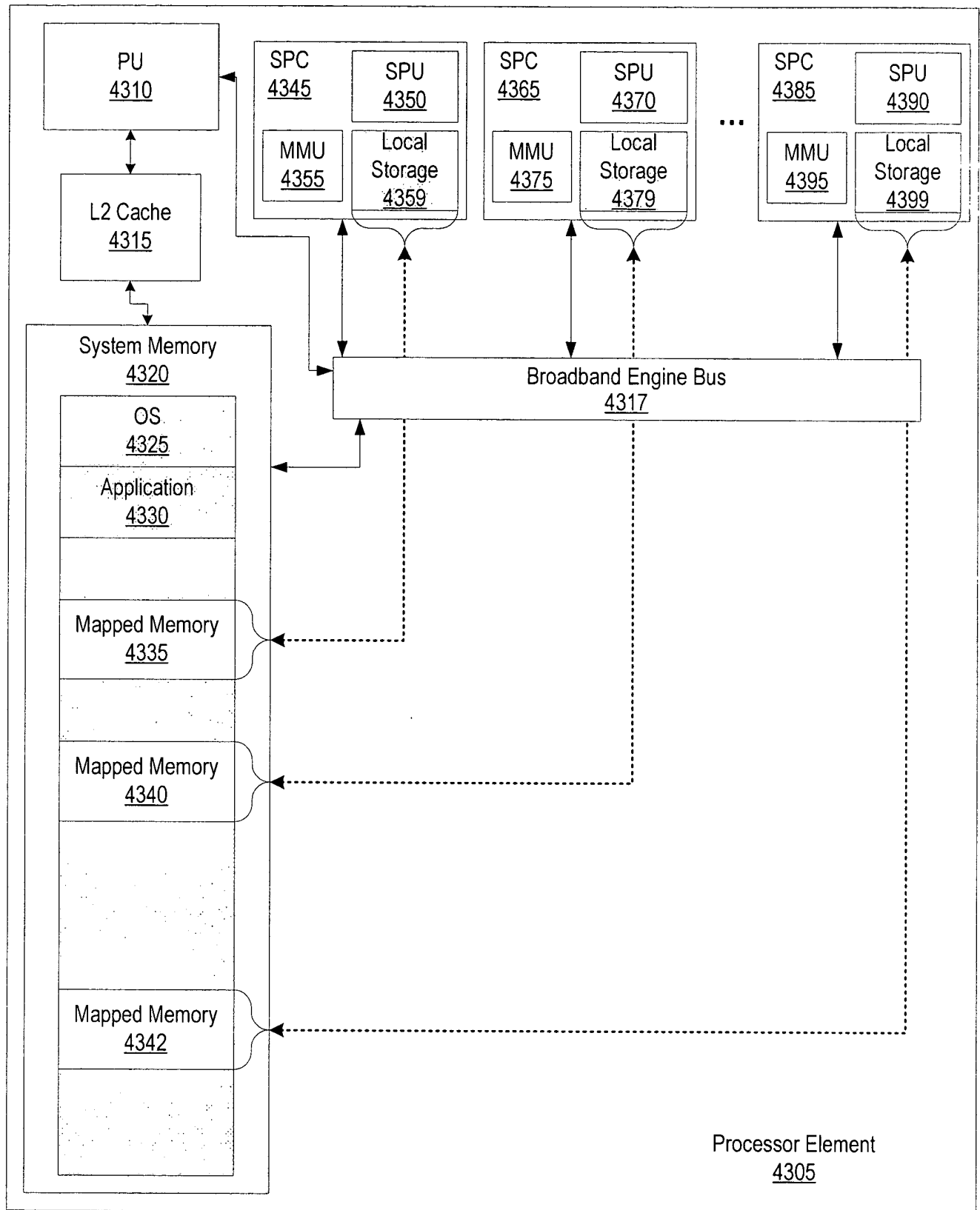
40 / 53

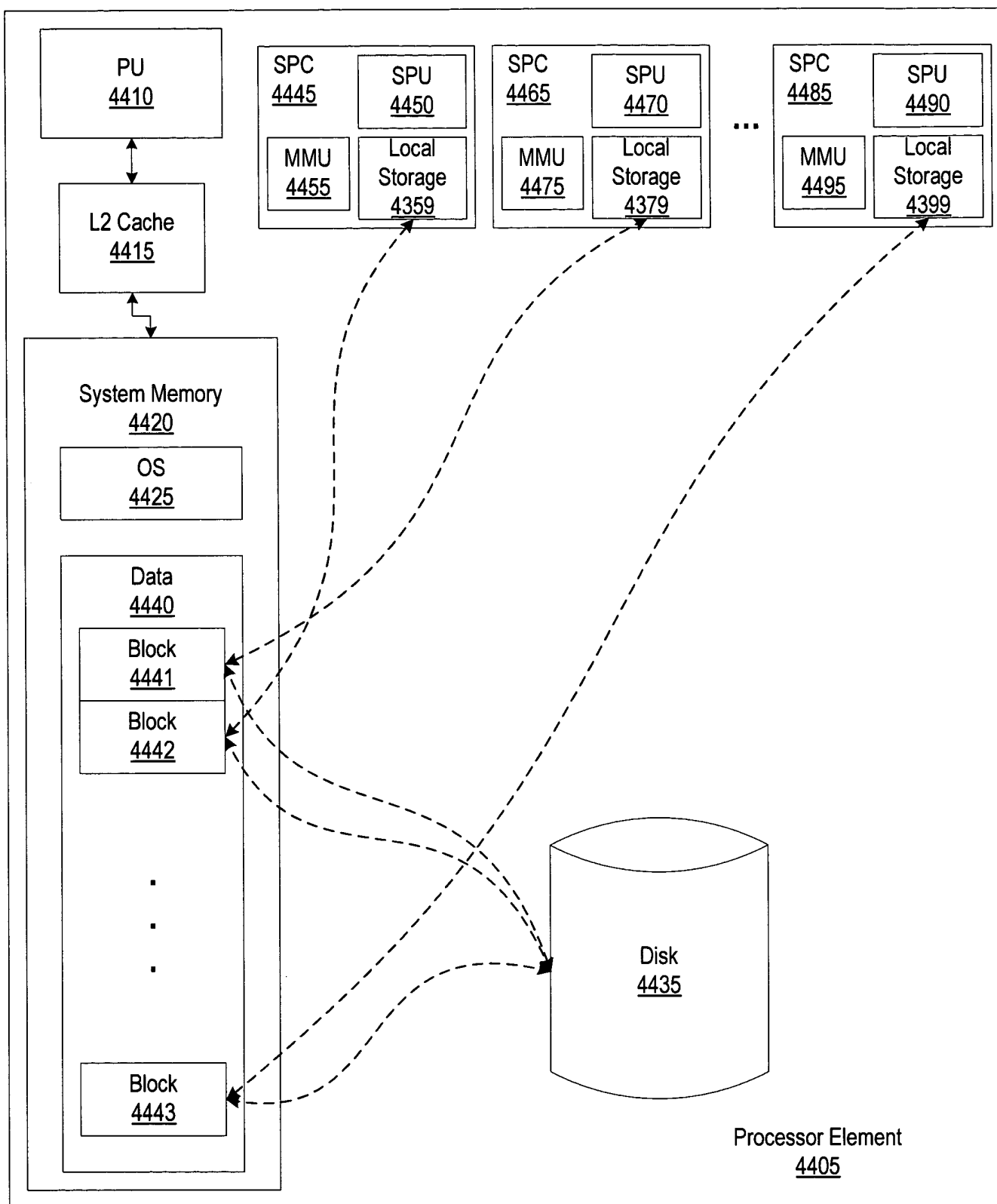
**Figure 40A****Figure 40B**

**Figure 41**

**Figure 42**

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**Figure 43**

**Figure 44**

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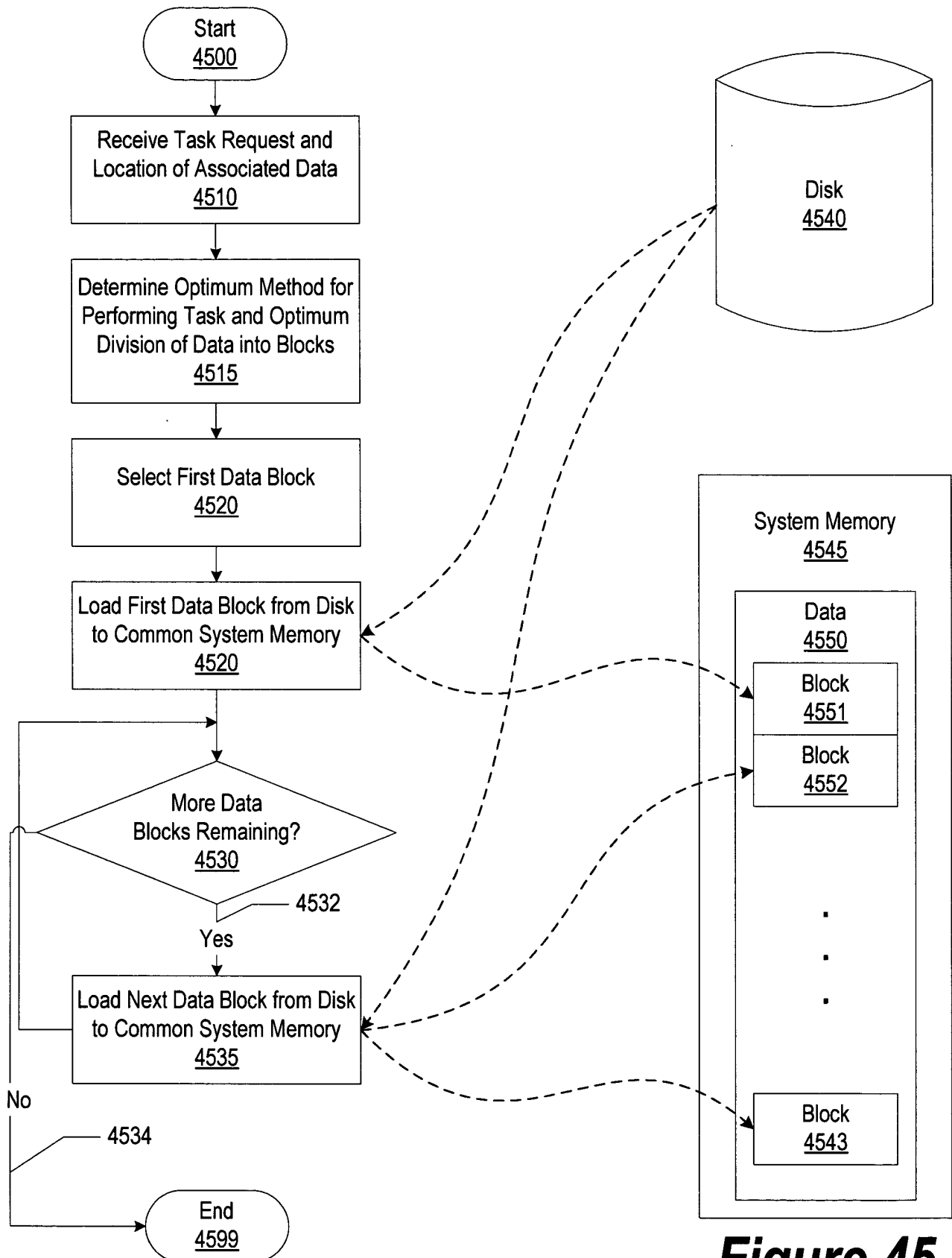
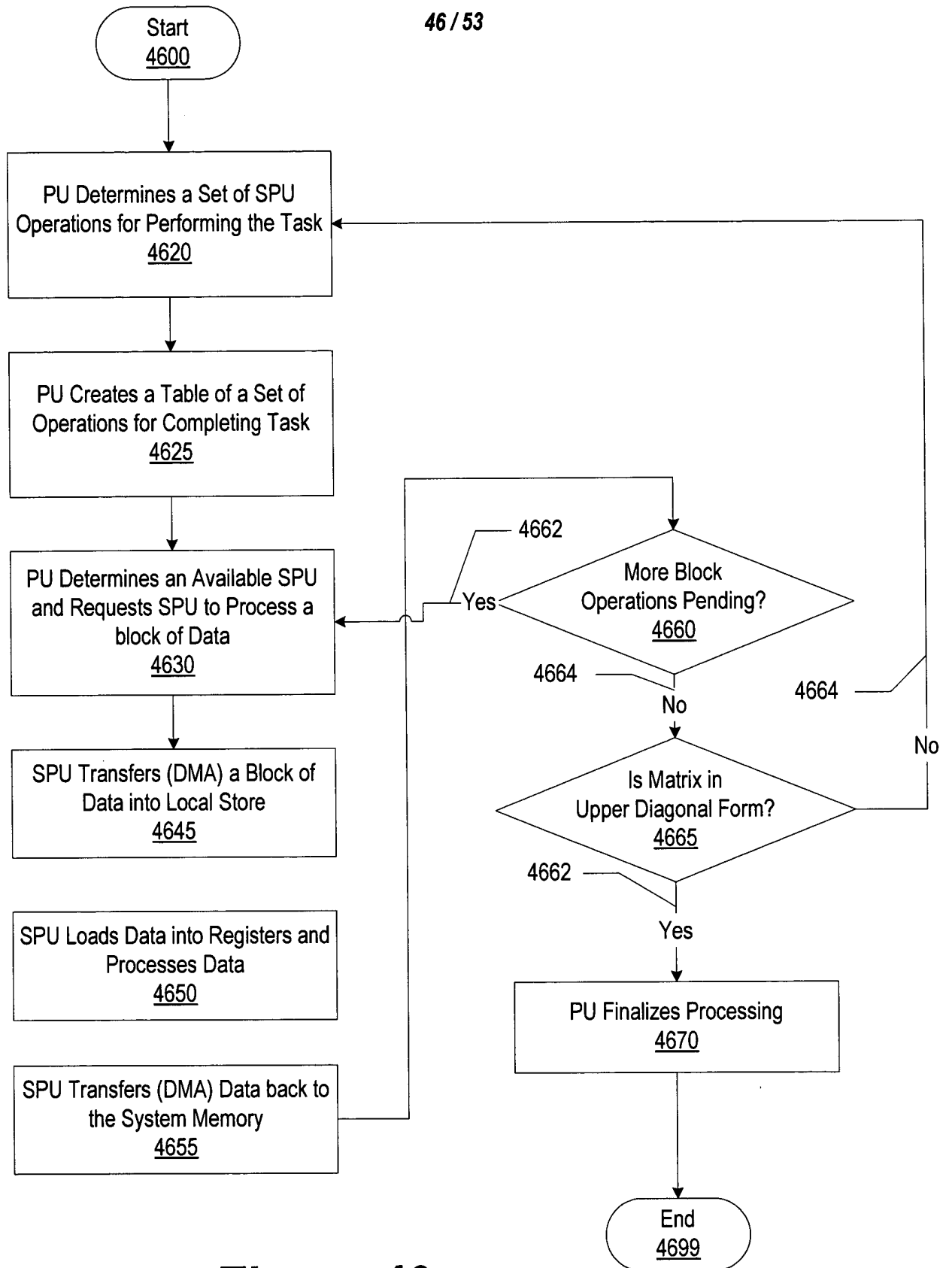


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**Figure 46**

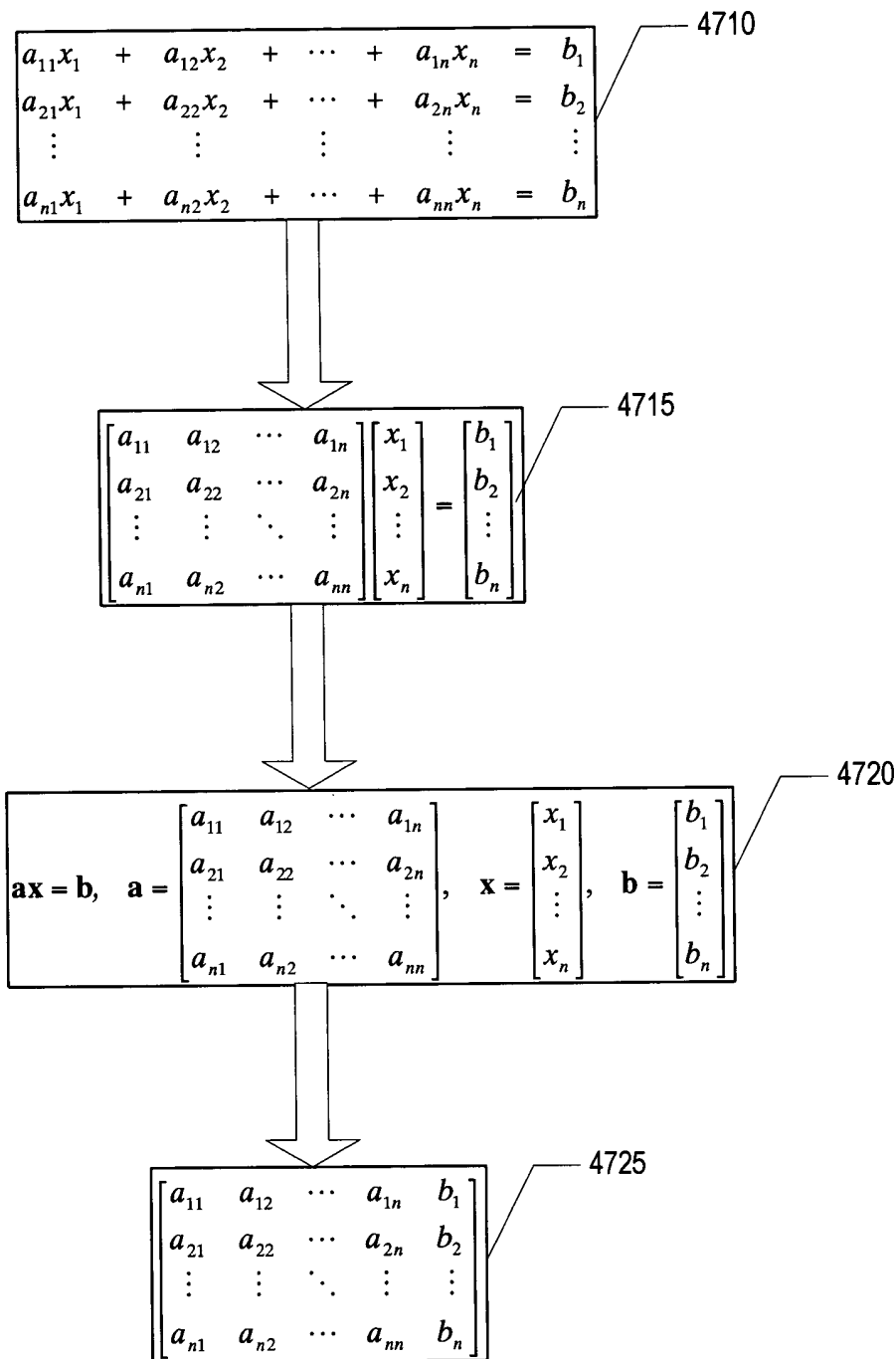


Figure 47

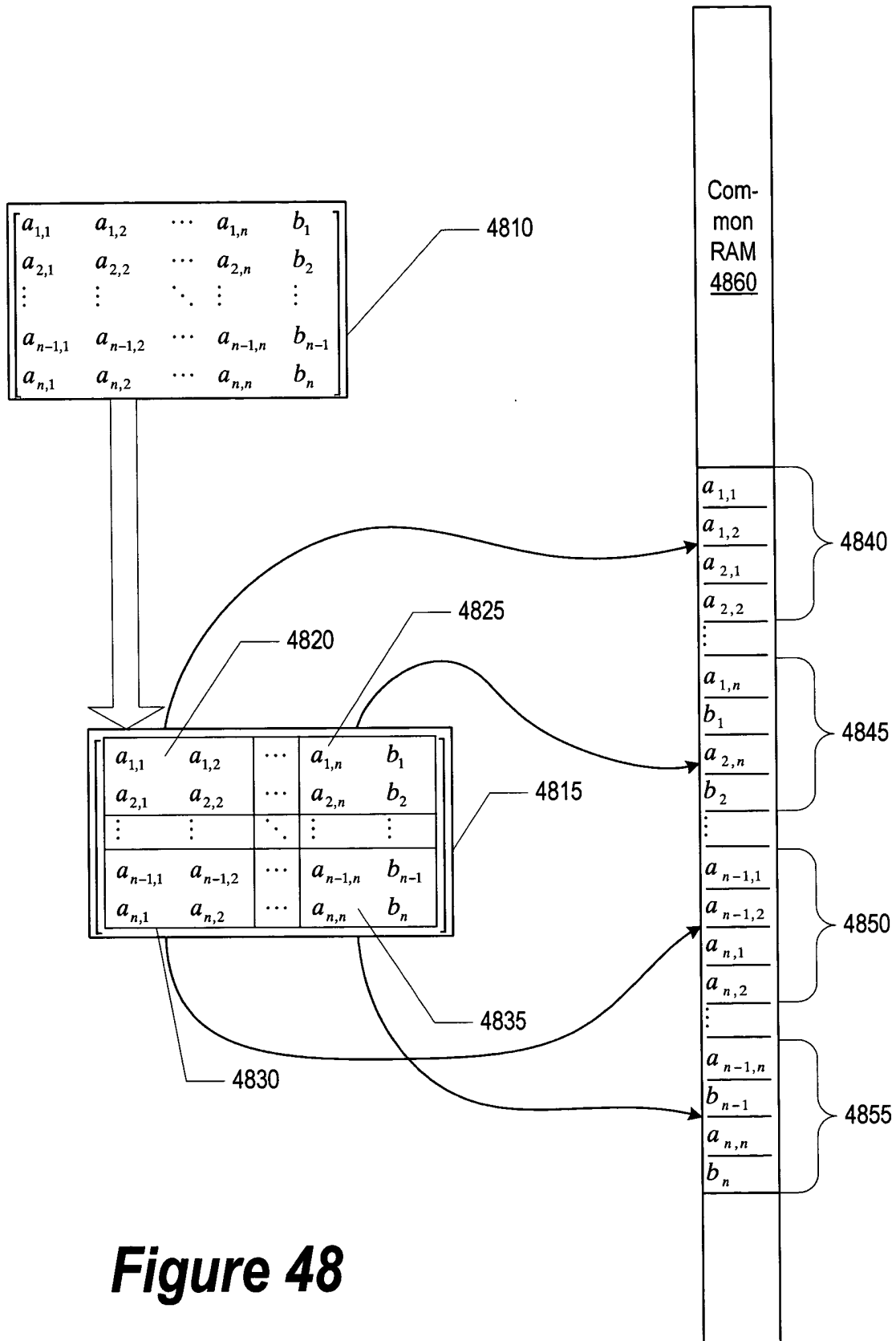


Figure 48

Operations	Block Operations	Completed?	SPU
Replace R1 with $4 \cdot R1 + R2$			
	Perform on Block A	Yes	SPU 5

	Perform on Block Z	In Progress	SPU 2
Replace R5 with $5 \cdot R8 +$			
	Perform on Block ...	No	?

4900

Figure 49

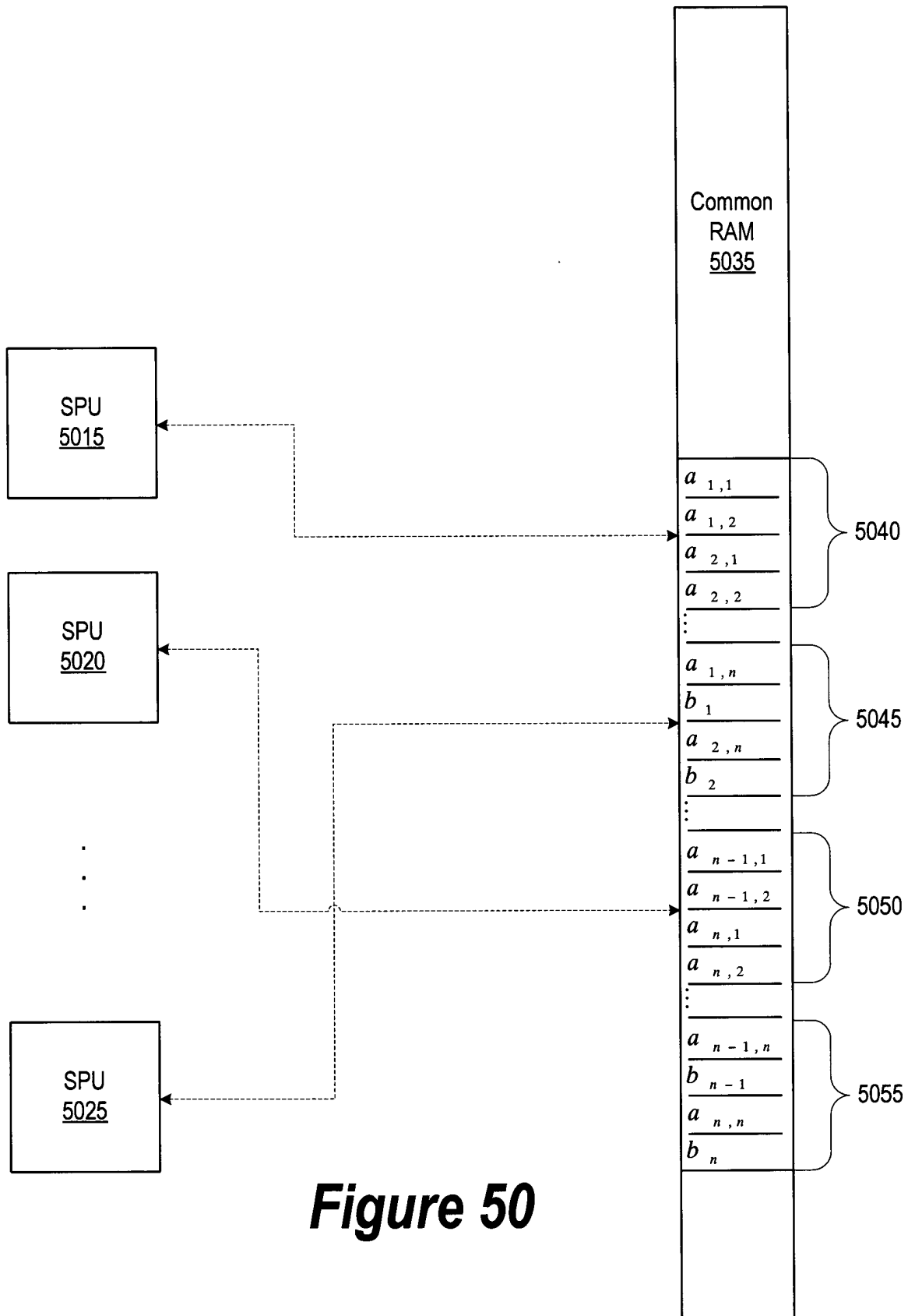
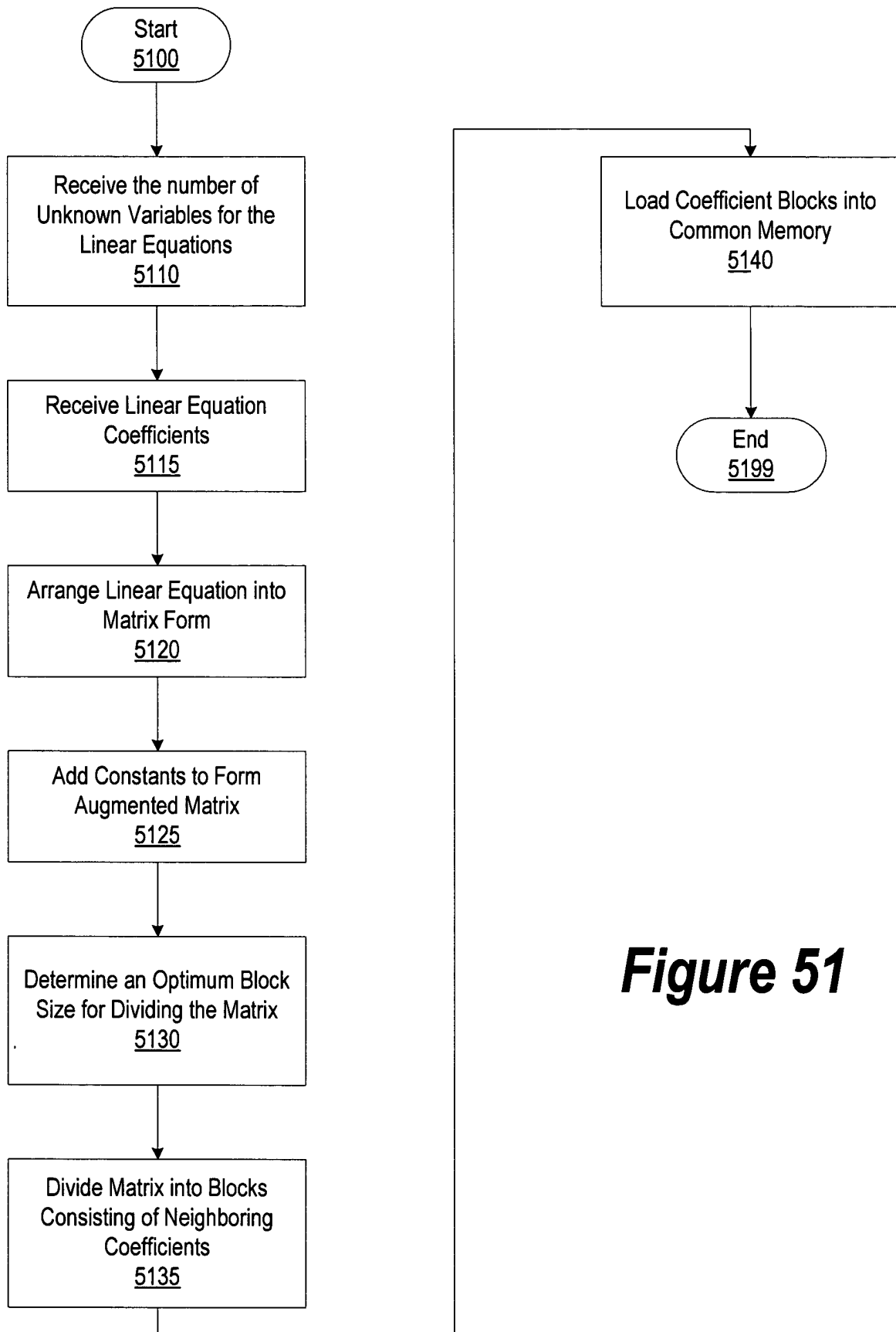
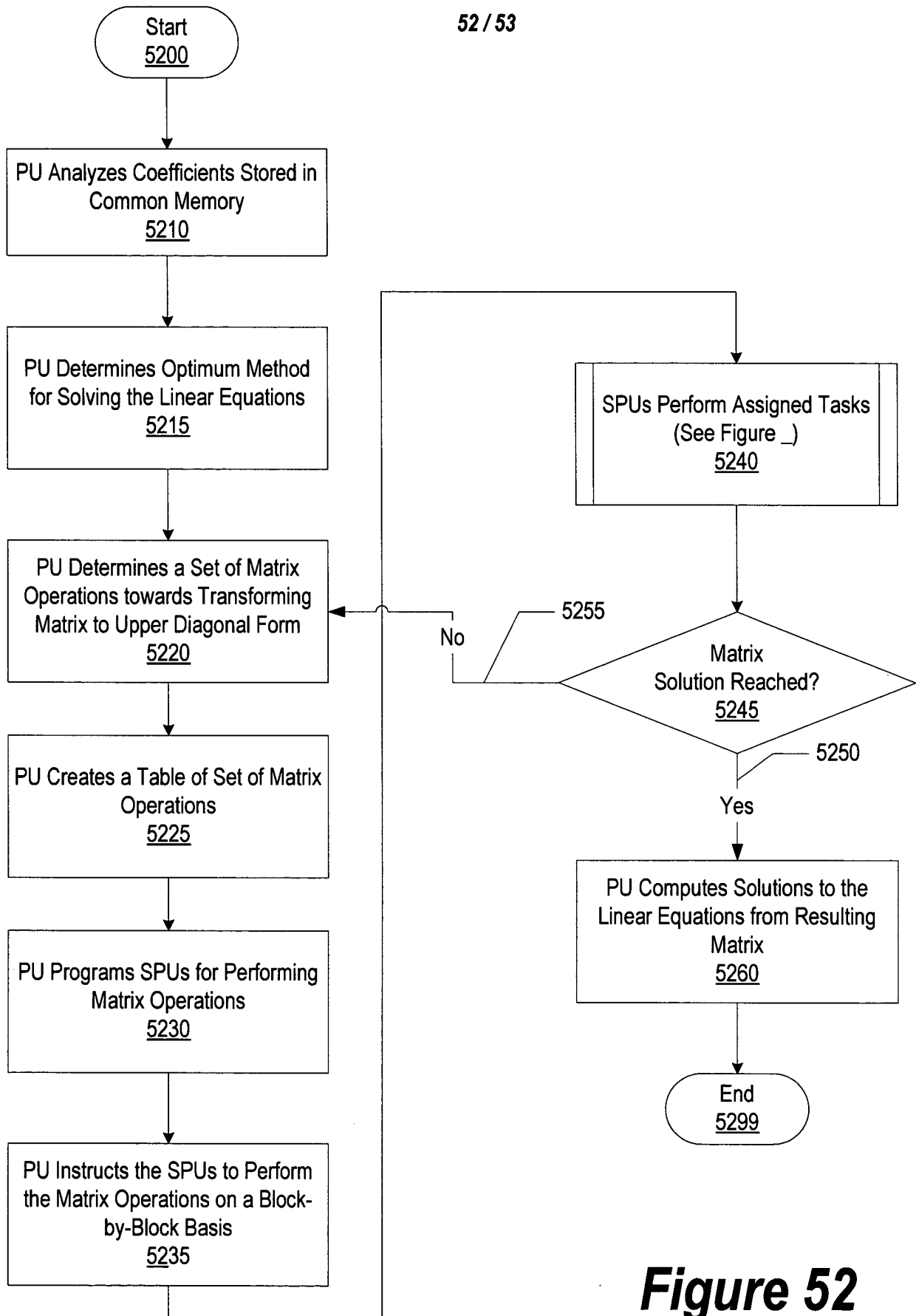


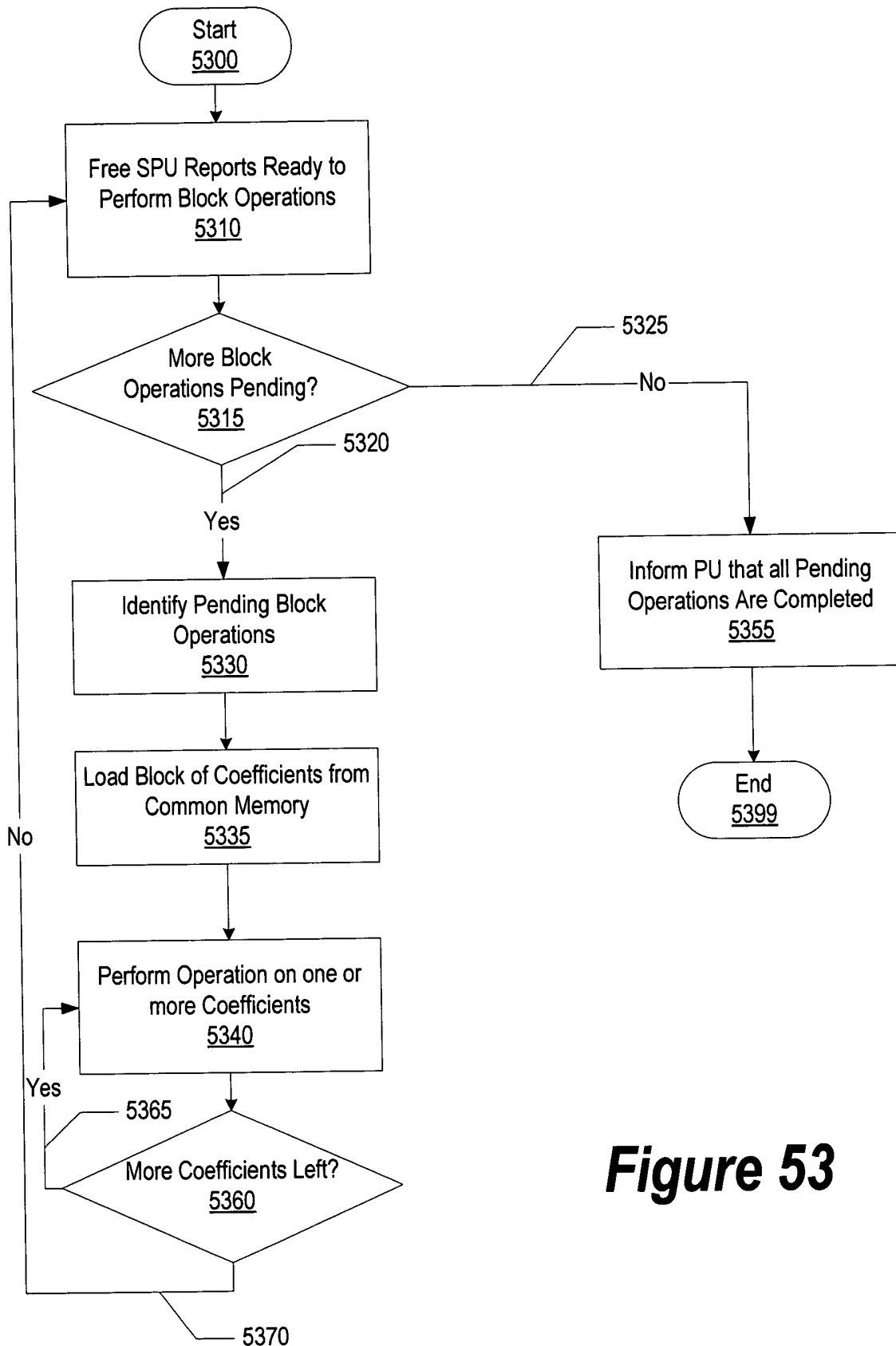
Figure 50

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**Figure 51**

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**Figure 52**

**Figure 53**